PCT

WORLD INTELLECTU/ Intern



INTERNATIONAL APPLICATION PUBLISHED

(51) International Patent Classification ⁶:

G06F 3/023, 15/163, 15/17, H05K 5/04, A1
H04L 1/00

(11) International Publication Number:

WO 96/02879

(43) International Publication Date:

1 February 1996 (01.02.96)

(21) International Application Number:

PCT/US95/09298

(22) International Filing Date:

18 July 1995 (18.07.95)

(30) Priority Data:

08/277,164

19 July 1994 (19.07.94)

us

(81) Designated States: CN, JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published

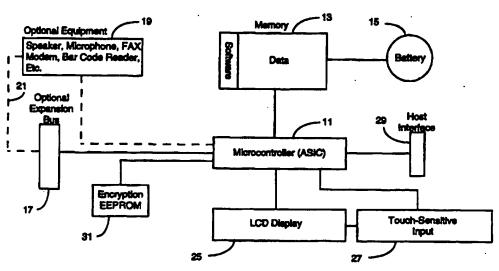
With international search report.

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(54) Title: MICRO PERSONAL DIGITAL ASSISTANT



(57) Abstract

A personal digital assistant module (10) with a local CPU (11), memory (13), and I/O interface (16) has a host interface (29) comprising a bus (26) connected to the local CPU (11) and a connector at a surface of the personal digital assistant (10) for interfacing to a bus connector of a host general-purpose computer (66), providing direct bus communication between the personal digital assistant (10) and the host general-purpose computer (66). In an embodiment, the personal digital assistant (10) also has a means for storing a security code. The personal digital assistant (10) according to the invention forms a host/satellite combination with a host computer (66) having a docking bay, wherein upon docking a docking protocol controls access by the host (66) to memory (13) of the personal digital assistant (10) also has an expansion port (20) connected to the local CPU (11), and expansion peripheral devices may be connected and operated through the expansion port (20).

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Micro Personal Digital Assistant

Field of the Invention

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This invention is in the area of portable computers and pertains more specifically to small portable computing devices known in the art as personal digital assistants.

Cross Reference to Related Documents

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The present application is a continuation-in-part of copending patent application S/N 08/144,231 and of copending patent application S/N 08/031,805.

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Background of the Invention

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Personal Digital Assistant (PDA) units, as of the date of this disclosure, enjoy a position of hope in the computer marketplace. Some believe this approach, a small, relatively inexpensive, and eminently portable computer unit, having software specifically written for tasks a user might expect to perform while travelling, will provide eminently useful and therefore salable computer products. Apple Computer, Hewlett Packard, and several other well-known computer manufacturers have made a considerable investment at no small risk in such systems.

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Given the new systems now introduced, and those coming, for what is now known about them, there are still a number of drawbacks and problems. For example:

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1. The PDA systems introduced are relatively costly, with starting prices ranging from several hundred dollars to two thousand dollars and more. At such prices, rivalling current pricing for desktop systems, the buying public may react negatively. It is true that prices will fall with increased manufacturing volume and competition, but

the high end start may well be rejected by potential users.

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- 2. The systems being offered are still relatively bulky, considering the limited range of tasks that may be accomplished. Most are certainly too big to be conveniently carried in a breast pocket. The Newton, manufactured by Apple Corporation, weighs about a pound and is approximately the size of a VHS video cassette.
- 3. A big drawback of the PDA systems being offered is the way they transfer data between a user's desktop unit, or other host, and the PDA. Known communication is by modem, by infrared communication, and by serial connection. These all require manipulation by a user, modulation on one or both ends of the communication path, and the like, which can be time-consuming, error-prone, and hardware extensive (expensive). Presently the Newton offers a modem and/or LED communication as an option, adding to the overall cost.
- 4. In known PDAs, software is typically recorded in ROM, so updating applications can be difficult, and sometimes impossible. This will be a problem because PDA users will not want the PDA to have the same capabilities at all times. Typical users will be people who travel and work while they travel. These users require different functions for a trip to Taiwan than for a trip to France, for example. What is needed is a quick and convenient means to update and substitute software.
- 5. Another difficulty is in the fact that the data files a user manipulates while travelling are typically data files also resident in a home unit, herein called a host unit, such as the user's office desktop machine or notebook or other portable computer. It is very troublesome to have two or more sets of critical data, with differences that one must remember to correct at an appropriate time. This can cause unending grief if files are not correctly updated. At best, current PDAs must use a relatively slow compressed bus to download and upgrade files. Typically this is done through a serial port, using a

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linking application like Laplink™.

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What is needed is a small and inexpensive PDA that has a range of features that eliminate the above-described risks and problems. This new unit needs to be smaller than those presently being introduced, such as about credit-card size, or perhaps modeled on the PCMCIA type II or type III standard form factors. It should be inexpensive enough to produce that at least a minimum version could be sold in the roughly \$100-\$200 range, so it will be a unit seen to be a relatively inexpensive necessity. A PDA unit of this sort is the subject of the present invention, and is termed by the inventors a micro-PDA, or μ PDA.

A very important feature of the μPDA in an aspect of the present invention is a direct parallel bus interface with a connector allowing the unit to be docked by plugging it into a docking bay in a host unit. Moreover, when the μPDA is docked in the host, there needs to be a means to effectively disable the CPU in the μPDA and to provide direct access to both the μPDA software and data storage by the host CPU. This direct access would provide immediate ability to communicate in the fastest available fashion between the μPDA and the host, and would also facilitate additional important features to be described below.

The μPDA also needs to have an optional compressed bus interface, including a connector separate from the host interface, so add-on devices may be utilized, such as a FAX modem, cellular communication, printer, and so on.

An additional feature that could be optionally provided in another aspect of the invention is an interface at the host to allow a user to select pre-arranged software mixes for loading to the μPDA . This feature comprises a set of control routines operating in conjunction with the host's display and input means, to allow the user to quickly select applications and perhaps data as well to be loaded to the μPDA satellite, to configure the smaller, more portable unit for specific itineraries and purposes.

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Another desirable feature is an ability to automatically update data files. In this aspect of the invention, with the μPDA docked, data on the host, if carrying a later date and/or time stamp than the data on the μPDA , would be automatically updated on the μPDA and vice-versa. When one returns from an excursion using the μPDA and docks the satellite at the host, the host gains access, determines the location of the latest files, and accomplishes the update. This feature needs to have some built-in user prompting to be most effective. It makes the μPDA a true satellite system.

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Summary of the Invention

In a preferred embodiment of the invention a personal digital assistant module is provided comprising an enclosure for enclosing and supporting internal elements, a microcontroller within the enclosure for performing digital operations to manage functions of the personal digital assistant module, and a memory means connected to the microcontroller by a memory bus structure for storing data and executable routines. There is a power supply means within the enclosure for supplying power to functional elements of the personal digital assistant module, a display means operable by the microcontroller and implemented on a surface of the enclosure, and input means connected to the microcontroller for providing commands and data to the personal digital assistant module. A host interface means comprising a host interface bus structure, which may be configured as a PCMCIA bus interface, is connected to the microcontroller and to a first portion of a host interface connector at a surface of the enclosure, and the host interface means is configured to directly connect the microcontroller to a compatible bus structure of a host computer.

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In one embodiment the personal digital assistant module has an expansion bus interface comprising an expansion bus structure

connected to the microcontroller and to a first portion of an expansion bus connector for connecting the microcontroller to a peripheral device. A wide variety of peripheral devices are provided for use with the personal digital assistant of the invention.

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In another aspect, the personal digital assistant module also has a nonvolatile storage device, such as an EEPROM connected to the microcontroller and containing one or more codes unique to the personal digital assistant, for uniquely identifying the personal digital assistant to digital devices connected on the host interface.

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In a preferred embodiment, the display and input means for the personal digital assistant are configured as an overlaid touch screen and LCD display on a surface of the outer case of the personal digital assistant. A pointer device implemented as a thumbwheel in one embodiment and as a pressure sensitive pad in another is provided as part of the input capability.

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The personal digital assistant module forms a unique combination with a general-purpose computer host having the personal digital assistant as a satellite unit. The host in this instance has a docking bay especially configured to dock the personal digital assistant, making a direct bus connection between the local CPU of the personal digital assistant and the CPU of the host. The host may be a desktop unit, a notebook computer, or a smaller portable like a palmtop computer. This combination provides power and convenience not before available.

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Many other digital devices are also provided according to various aspects of the invention, such as modems, scanners, data acquisition peripherals, cellular phones, and a software vending machine, and all of these devices may be appended to the personal digital assistant by the expansion bus interface or, in many cases, by the host interface.

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The personal digital assistant provided according to embodiments of the present invention is a unit more compact than

conventional PDAs. It represents a new dimension in computer application and applicability, in a form promising to be eminently usable by and useful to almost everyone; and at a price easily affordable. It solves the communication problem intrinsic to personal digital assistants relative to larger and more powerful computers, with a unit that fits into a user's breast pocket, and at a very low price.

Brief Description of the Drawings

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Fig. 1A is an isometric view of a μ PDA according to an embodiment of the present invention.

Fig. 1B is a plan view of the μ PDA of Fig. 1A.

Fig. 2 is a cross-sectional view of the μPDA of Figs. 1A and 1B.

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Fig. 3 is a block diagram of the μPDA of Fig. 1A and some peripheral elements.

Fig. 4 is a more detailed plan view of the μPDA of Fig. 1A showing in particular an LCD display and touch screen user interface in an aspect of the present invention.

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Fig. 5 is an isometric view of a μ PDA and a host notebook computer in an aspect of the present invention, with the μ PDA about to be docked in a docking bay of the notebook computer.

Fig. 6 is a block diagram of a µPDA docked in a docking bay of a host computer according to an embodiment of the present invention.

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Fig.7 is a logic flow diagram of the steps in docking a μPDA in a host computer according to an embodiment of the present invention.

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Fig. 8 is an isometric illustration of a μ PDA software vending machine in an aspect of the present invention.

Fig. 9 is a top plan view of a μPDA enhanced user interface according to an embodiment of the present invention.

Fig. 10 is a top plan view of a μPDA with a microphone in an embodiment of the present invention.

- Fig. 11 is an isometric drawing of a μPDA docked in a dedicated cellular or cordless telephone according to an embodiment of the present invention.
- Fig. 12 is a plan view of a μ PDA with a speaker and pager interface according to an embodiment of the present invention.
- Fig. 13 is a plan view of a μPDA with an infrared communication interface according to an embodiment of the present invention.
- Fig. 14 is a plan view of a μPDA with a scanner attachment according to an embodiment of the present invention.
- Fig. 15 is a plan view of a μPDA with a fax-modem attached according to an embodiment of the present invention.
- Fig. 16 is a plan view of a μPDA with a printer adapter interface according to an embodiment of the present invention.
- Fig. 17 is an isometric drawing of a μ PDA docked in a barcode reader providing a data acquisition peripheral according to an embodiment of the present invention.
- Fig. 18 is an isometric view of a μ PDA with a solar charger according to an embodiment of the present invention.
- Fig. 19 is a plan view of four μPDAs interfaced to a dedicated network console providing inter-PDA communication according to an embodiment of the present invention.
- Fig. 20 is an isometric view of a μ PDA according to the invention connected by the expansion port to a standard-sized keyboard.
- Fig. 21A is a plan view of a flexible, roll-up keyboard according to the present invention.
 - Fig. 21B is an elevation view of Fig. 21A.
- Fig. 21C shows the flexible keyboard of Fig. 21A rolled into a compact cylinder and secured by a band.
- Fig. 22 is an isometric view of a single key cell in the flexible keyboard according to the embodiment of Fig. 21A and 21B.
 - Fig. 23A is a section view of the key cell of Fig. 22 taken

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along line 23A-23A of Fig. 22.

Fig. 23B is a plan view of the lower layer at the key cell of Fig. 23A in the direction of arrow 29.

Fig. 24A is a plan view of the lower layer at the position of the Shift key.

Fig. 24B is a plan view of the upper layer from the same vantage as for Fig. 24A.

Fig. 25A is an isometric view of the end of the flexible keyboard where the control circuitry is housed, showing the layer separated to illustrate internal details.

Fig. 25B shows the control circuitry module at an angle of about 90 degrees from the view of Fig. 25A.

Fig. 25C is a view of the control module in the direction of arrow 75 to illustrate the arrangement of contact pads relative to the module.

Fig. 25D is a section view taken along line 25D-25D of Fig. 25A with the two layers joined.

Fig. 26A shows an alternative embodiment that communicates with a suitably equipped computer without a cable.

Fig. 26B shows an alternative embodiment wherein the wiring matrix is connected directly to a computer through a connecting cable.

Fig. 27A shows a flexible keyboard according to the present invention connected to a computer by a transmission cable.

Fig. 27B shows a flexible keyboard according to an embodiment of the invention wherein code is transmitted by a magnetic field.

Fig. 28 is a perspective view of a computer with a keyboard according to the present invention.

Fig. 29 illustrates a key switch matrix interfaced to a microprocessor and a magnetic transmitter according to the present invention.

Fig. 30 illustrates a receiver and demodulation circuitry for

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receiving magnetically encoded scan codes and reconstructing the associated digital scan codes therefrom.

Fig. 31A is an exemplary trace of emf in a receiving loop according to an embodiment of the invention.

Fig. 31B is a serial digital scan code as reconstructed from the magnetically encoded code of Fig. 31A.

Description of the Preferred Embodiments

Fig. 1A is an isometric view of a μPDA 10 according to an embodiment of the present invention. In this embodiment the unit is modeled on the PCMCIA standard Type II form factor, having a height D1 of about 5mm. Body 12 is described in further detail below, and has a female portion 14 of a connector recessed at one end for engaging a mating male portion of the connector in a host computer, connecting the μPDA internal circuitry directly with a host internal bus. The host unit may be a notebook computer having a docking bay for the μPDA. Docking bays may be provided in desktop and other types of computers, and even in other kinds of digital equipment, several examples of which are described below.

Still referring to Fig. 1A, in this embodiment there is a combination I/O interface 16 implemented on one side of the μ PDA, comprising a display overlaid with a touch-sensitive planar structure providing softkey operation in conjunction with interactive control routines operable on the μ PDA in a stand-alone mode.

Although not shown in Fig. 1A, there may also be guides implemented along the sides of the case of the device for guiding the module in and out of a docking bay in a host computer unit. There may also be one or more mechanical features facilitating engagement and disengagement of the module in a docking bay.

Fig. 1B is a top plan view of the µPDA of Fig. 1A, showing a

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thumbwheel 18 implemented in one corner of the μPDA . The thumbwheel in this embodiment is an input device capable of providing input with both amplitude and directional characteristics, and in some cases rate characteristics as well. The thumbwheel has many uses in combination with the μPDA and I/O interface 16. One such use is controlled scrolling of icons, characters, menus, and the like on the display of the device. The thumbwheel provides many of the functions of a pointer device.

In this embodiment of the μPDA a second external connector portion 20 is provided. This connector portion is for engaging peripheral devices as part of an expansion bus interface.

Fig. 2 is a simplified cross-sectional view of a means for constructing a μ PDA according to the present invention in a Type II PCMCIA, or other relatively small package. ICs 34 are encapsulated in a conformal material 36, and interconnection is accomplished by traces on a flexible polymer film 32 shown as overlaying the encapsulated structure. In this structure the ICs are not packaged in the conventional manner having solder leads for assembly to a printed circuit board. Rather, connections are made directly between the solder pads on the chip and the traces on the Kapton film. Also there is no intention to relate ICs indicated by element No. 34 with specific functional ICs in a μ PDA. This cross-section is illustrative of a method of construction only.

In this compact construction there may also be traces on the side of film 32 away from the interconnections for the CPU and memory for connection to other elements, such as display 25 and touch-sensitive screen 27.

LCD display 25 is implemented on one side of the µPDA, and touch-sensitive interface 27 is provided overlaying at least a portion of the LCD display. A metal casing 38, or other suitable material or combinations of material, surrounds the internal components and conforms to Type II PCMCIA form factors. This simplified cross-section illustrates some of the principles of construction that can

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allow the needed components to be inexpensively fitted into the small form factor needed. In another embodiment the µPDA is implemented in the form factor of a type III (10 mm thick) PCMCIA unit, using relatively conventional technology, such as PCB technology, rather than the encapsulated construction described immediately above. Various other constructions, form factors, and combinations are possible, as well.

Fig. 3 is a simplified electrical block diagram of the μPDA of Figs. 1A, 1B and 2. A unique microcontroller 11 acts as the CPU of the μPDA in the stand-alone mode, that is, when the μPDA is not docked in a host unit. When the μPDA is docked in a host computer, microcontroller 11 acts as a slave unit, granting bus control to the CPU of the host. In docked mode, the CPU of the host thus gains control of the memory contents of the μPDA, subject in most cases to security procedures which are described below. Thus the host computer can transfer data and software into and out of a docked μPDA memory. In other embodiments many other cooperative operating modes may be accomplished between the two CPUs and accessible memory devices.

Memory 13 is preferably a nonvolatile device from 1 to 2 megabytes in this embodiment, and both control routines for applications and data files are stored in this memory. Memory 13 may be flash memory, CMOS ROM, CMOS RAM with battery, or a combination, with the software stored in ROM and the data in the flash memory. The memory device is interfaced to microcontroller 11 via a dedicated bus structure 17, and microprocessor 11 is configured to drive memory bus 17.

A battery 15 is the power source in the stand-alone mode, and may be recharged in one or more of several ways. The power traces are not shown in Fig. 3, but extend to all of the powered devices in the µPDA module. When the unit is docked in the host, the host power source may be connected to pins through the host interface to recharge the battery. Alternatively, an attached means such as a solar

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panel may be configured to charge the battery and/or provide power to the μPDA . A solar panel for power is described elsewhere in this disclosure. Also the battery may be easily removed for periodic replacement.

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Host bus connector 14 is a part of a host interface which comprises a bus structure 26 for providing connection to the host in docked mode, as described above. In a preferred embodiment, the host interface is according to PCMCIA Type II, Rev. 3 standard, which is capable of communication either in PCMCIA mode or in a mode similar to PCI mode. PCI mode refers to a high-speed intermediate bus protocol being developed by Intel corporation, expected to become a standard bus architecture and protocol in the industry. The physical interface at the host in this embodiment is a slot-like docking bay, as is typical of know docking bays for PCMCIA devices. This docking bay may be implemented as a docking box, a built-in unit like a floppy-drive unit, or it may take some other form.

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Connector portion 20 is a part of the expansion bus interface described above, comprising a dedicated bus structure 40 connected to microcontroller 11. This interface can be implemented in a number of different ways. The purpose of the optional expansion bus interface is to connect to optional peripheral devices, such as a printer, a FAX modem, a host cellular phone, and others. The expansion bus interface is not an essential feature in a minimum embodiment of the present invention, but provides vastly enhanced functionality in many embodiments.

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The expansion interface can take any one of several forms. A preferred form is an extended enhanced parallel port and protocol based on an invention by the present inventors disclosed in a copending patent application. Another form is an indexed I/O port having 8-bit address and 8-bit data capability. The requirement of the expansion port is that the connection and communication protocol be compatible with expansion devices, such as telephone modems, fax

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modems, scanners, and the like. Many other configurations are possible.

Optional equipment such as devices listed in box 19 may be connected for use with the μ PDA through the expansion bus. Selected ones of such devices may also be built in to the μ PDA in various embodiments, providing variations of applicability. In the former case, connection is through path 21 and the expansion bus interface via connector portion 20. In the built-in case, connection is in the interconnection traces of the μ PDA as indicated by path 23.

I/O interface 16 (also Fig. 1B) is for viewing µPDA application-related data and for touch-sensitive input via softkeys. By softkeys is meant assignment by software of various functions to specific touch sensitive screen areas, which act as input keys. Labels in I/O interface 16 identify functionality of the touch-sensitive areas in various operating modes according to installed machine control routines. LCD display 25 and the touch-sensitive area 27 together form the combination I/O interface 16 described also above.

In some embodiments of the present invention, data and program security is provided comprising an Electrically Erasable Programmable Read Only Memory (EEPROM) 31, which is connected by dedicated communication lines to microcontroller 11. EEPROM 31 holds one or more codes installed at the point of manufacturing to provide security for information transfer between a host and a µPDA. The purpose is to control access by a host to the memory contents of a µPDA, so each µPDA may be configured to an individual. To accomplish this, docking and bus mastering machine control routines are initiated at the point of docking, and this security process is described in more detail below. In other embodiments, security codes may be provided by a Read Only Memory (ROM) chip or other permanent or semi-permanent memory source.

Fig. 4 is a plan view similar to Fig. 1B, of a μPDA, showing in particular I/O interface 16. The size and location of I/O interface 16 may vary, but in general occupies a major portion of one of the

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sides of the module. In one embodiment I/O interface 16 comprises an LCD display with a resolution of 256 by 144 pixels in a screen size that displays 32 by 12 characters. Each character in this embodiment is displayed in an area eight pixels wide and twelve pixels high. In another embodiment, the pixel resolution is 320 by 200, which corresponds to 40 by 16 characters.

The touch-sensitive areas of the touch-sensitive screen correspond to the character areas of the display. By touching an area with a finger or stylus, data can be entered quite quickly and with minimal CPU demand.

At one corner, thumbwheel 18 provides a two-directional means of controlling the configuration of the display according to installed control routines. A menu 70 is configured at one side to represent the current status of any application in progress and to provide appropriate user menu selections. In a preferred embodiment input from thumbwheel 18 is used for scrolling through menu 70, and active areas may be indicated by a cursor. A user makes a menu selection by pressing the appropriate touch-sensitive area. A specific input may be provided to cause the menu area to be displayed on either side of the display according to a user's preference.

Specific characters are displayed in this embodiment in a region 74, with each character area associated with a touch-sensitive input area. As region 70 dedicated to selectable characters is much too small to display all characters of a standard keyboard, input from thumbwheel 18 allows a user to pan region 74 displaying an entire virtual standard keyboard. Movement of thumbwheel 18 in one direction pans the character region horizontally, and movement in the other direction pans the character region vertically. When an end is reached the window pans onto the virtual keyboard from the other end. In this manner, a user may quickly pan the character window to display an entire standard keyboard, and make selections with a finger or a stylus. Of course, it is not required that a virtual keyboard be

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laid out for access in the format of a standard keyboard. Characters and punctuation, etc., could just as simply be displayed in a single strip along a region of the display, and scrolled by input from the thumbwheel or other pointer-type input device.

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In this embodiment, to avoid delays caused by panning, if the thumbwheel is rotated quickly the character window jumps rather than scrolling to speed up the interface. In addition, menu 70 may optionally provide for a character display in different fonts and sizes, although a single font is preferred to minimize memory demand. It will be apparent to those with skill in the art that there are many alternatives for character selection and display, and many ways thumbwheel 18 may be configured to allow for scrolling and panning.

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A document window 72 is provided in this embodiment at the top or bottom of I/O interface 16. A cursor locates the active position within the document for editing purposes. Menu 70 provides selection of available fonts, and input by thumbwheel 18 controls cursor movement over the document. As a document will in almost all cases be much larger than the display capability of region 72, it is necessary to pan the document window in essentially the same manner as the keyboard window is panned. For example, rotating thumbwheel 18 in one direction may display horizontal strips of a document, while rotating the thumbwheel in the opposite direction moves the window vertically strips of the same document.

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A soft key or optional hard key may be configured to switch between the document and keyboard window, and the same or another key may be configured to switch between scrolling left or right, up or down, document or keyboard. A switch key may be used to change the thumbwheel mode of operation. A switch key may also be used in combination with a floating pointer to select characters and menu items. In this embodiment, the user can keep his or her hands relatively stationary on just the thumbwheel and the switch key, making all possible selections. Use of a switch key in combination

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with a floating pointer facilitates the use of small fonts. A switch key may also be incorporated as an additional hard key in a convenient location on the case 12.

It will be obvious to a person skilled in the art than there are numerous ways to combine menu selections, switching keys and I/O configurations to provide a user-friendly user interface. A further embodiment of the present invention provides an I/O set-up application wherein a user may completely customize features of I/O area displays.

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There are other sorts of mechanical interfaces which may be used to provide pointer-style input in different embodiments of the invention as alternatives to the thumbwheel disclosed. One is a four-way force-sensitive mouse button and a selector button, which may be located at opposite ends of case 12 below I/O interface 16. Each button is designed to be operated by one finger. The four-way force-sensitive mouse button can provide menu scrolling of a cursor and panning and/or indexing of keyboard and document windows, while the selector button is used to select and edit according to position of a cursor. This configuration minimizes hand movement and keeps the I/O area clear for viewing.

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Implementation of thumbwheels, pressure-sensitive switches and buttons, and the like, are known in the art, including the translation of mechanical motion and pressure to electrical signals and provision of such signals to a microcontroller. For this reason, details of such interfaces are not provided in this disclosure. Combinations of such inputs with displays and input areas may, however, be considered as inventive.

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Fig. 5 is an isometric drawing of a μ PDA 10 in position to be docked in a notebook computer 172 via a Type II PCMCIA docking port 105 according to an embodiment of the present invention. As further described below, once the μ PDA is docked, it is activated and a procedure is initiated with the host computer to manage

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communication and verify memory access rights (security).

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Access rights are considered important by the inventors for a number of reasons. Firstly, through the expedient of one or more specific codes, unique to each µPDA, a user may protect files stored in his module from access by unauthorized persons. The code can be used both to control access to data and files via I/O interface 16, and also through the host bus interface, so data and files may be secure from access by an unauthorized host system.

In the former case, when a µPDA is powered up, an application routine can query the user for an access code to be entered at I/O interface 16 Fig. 4). If the code is not entered properly, access is denied, and power goes off. Codes for the purpose are stored in EEPROM 31 (Fig. 3), or in whatever ROM device may be devoted to the purpose. In some embodiments, the code may by mask-programmed at manufacture, so it is not alterable. In others, the code may be accessible and changeable by special procedures in the field.

In the case of host communication, it is possible that a portable or desktop computer, or some other device, may have a docking port physically configured to receive a μPDA , yet not be configured to communicate with the μPDA . This certainly might be the case where the μPDA is in the PCMCIA form. For purposes of disclosure and description, this specification terms such a unit a generic host. If the unit is configured to communicate with a μPDA it is an enabled host. If a host is configured for full access to a particular μPDA , it is a dedicated host.

If a docking unit is a generic host, there will be no communication unless the person presenting the μPDA provides the control routines to the host. This may be done for a generic host such as by transfer from a floppy disk, from a separate memory card through the docking port, or, in some embodiments, the communication software may be resident in memory 13 (Fig. 3) of a docked μPDA , transferrable to the host to facilitate further communication.

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If the docking unit is in fact an enabled host, or is configured after docking to be an enabled host, the stored code or codes in EEPROM 31 (or other storage unit) may be used to verify authorization for data and program transfer between the host and a μ PDA. In one embodiment this procedure is in the following order: First, when one docks a μ PDA in a compatible docking port, certain pin connections convey to both the μ PDA microcontroller and to the host CPU that the module is docked. Assuming an enabled host, the fact of docking commences an initialization protocol on both systems.

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In most embodiments, if the docking unit is a non-host, that is, it is not capable of communication with the docked module, nothing happens, and the user may simply eject the docked module. If the computer is an enabled host, an application is started to configure host access to the μ PDA's data files through the μ PDA microcontroller. A user interface, described more fully below for a particular embodiment, is displayed on the host monitor 104 (Fig. 5). The host interface menu, as well as other application menus, may be formatted in part as a display of the μ PDA I/O interface 16 as seen in Fig. 4 and described in accompanying text. In some embodiments, the docked μ PDA can be operated in situ by manipulating the input areas of the μ PDA displayed on the host's screen.

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If the host is not a home unit for the docked module, that is, the host does not have matching embedded ID codes to those stored in the docked module, a visitor protocol is initiated. In this event, a visitor menu is displayed on host display 104 for further input, such as password queries for selections of limited data access areas in the docked module. In this case, too, a user may gain full access to the docked module's memory registers by entering the proper password(s).

If the host is a fully compatible host home unit, full access may be immediately granted to the host to access memory contents of the docked module, including program areas; and both data and programs may be exchanged.

In any case, when the µPDA is ejected or otherwise removed

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from the docking port, the on-board module microcontroller again gains full control of the internal μPDA bus structures.

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Fig. 6 is a simplified block diagram of a μPDA docked in a host computer, and Fig. 7 is a basic logic flow diagram of the steps involved in docking a μPDA in a host computer 66 according to an embodiment of the present invention. Host computer 66 is represented in a mostly generic form, having a host CPU 24, and input device 60, such as a keyboard, a mass storage device 28, such as a hard disk drive, and system RAM 62. It will be apparent to those with skill in the art that many hosts may have a much more sophisticated architecture, and the architecture shown is meant to be illustrative.

When a μ PDA unit is docked, connector 14' in Fig. 6 comprises portion 14 shown in Figs. 1B and 3 and a mating connector portion for engaging portion 14 in port 105 (Fig. 5). The engagement of the separate portions of the connector cause bus 26 in the μ PDA and bus 26' in the host to become directly connected. There is then a direct bus path between microcontroller 11 and host CPU 24 (Fig. 6).

As previously described there is a pin configuration (not shown) in connector 14 dedicated to signalling that a module is docked. In Fig. 7, step 42 represents insertion of a µPDA module into the docking port. At step 44 the signalling pin configuration signifies physical docking is accomplished. At step 46 host interface bus 26 is activated, including the mated host bus 26' in the host.

At step 48 (Fig. 7) microcontroller 11 in the μPDA starts a preprogrammed POST procedure. Microcontroller 11 in this embodiment has a page of RAM 68 implemented on the microcontroller chip. In other embodiments RAM may be used at other locations. At step 50, the POST routine loads a bootstrap program to RAM 68, which includes a code or codes for security matching. This code or codes comprise, for example, a serial number.

At step 54 the bootstrap program begins to execute in microcontroller 11, and at step 56 the microcontroller looks for a

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password from the host on host interface bus 26 (Fig. 6).

The fact of docking, assuming an enabled or dedicated host, also causes a communication routine, which may be accessed from, for example, mass storage device 28 at the host, to display a user interface on monitor screen 104 of the host unit, as partly described above. It is this communication program that makes a generic host an enabled host.

Assuming an enabled, but not dedicated, host, the user interface will query a user for input of one or more passwords, after successful entry of which the host will pass the input to microcontroller 11 for comparison with the serial number and perhaps other codes accessed from EEPROM 31 in the bootstrap of the µPDA.

According to the codes passed from the host to the docked module, microcontroller 11 will allow full access to memory 31 at function 52, Fig. 7, for the host CPU, or limited access at some level at function 58, defined by received codes (or no matching code at all).

The access protocols and procedures allowing partial or direct access to μ PDA memory 13 are relatively well known procedures in the art, such as bus mastering techniques, and need not be reproduced in detail here. In addition to simple comparison of codes, there are other techniques that may be incorporated to improve the integrity of security in the communication between a μ PDA and a host. For example, within the limitation of storage capacity of the EEPROM or other nonvolatile source, executable code might also be uploaded to onboard RAM 68, or code keys to be used with executable code from other sources, or relatively simple maps re-allocating memory positions and the like, so each μ PDA may be a truly unique device.

There are additional unique features provided in one aspect of the invention as part of the communication routines introduced above. One such feature is automatic updating and cross-referencing of existing files and new files in both computers, under control of the host system, with the host having direct bus access to all memory

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systems. Auto-updating has various options, such as auto-updating by clock signature only, flagging new files before transfer, and an editing means that allows the user to review both older and newer versions of files before discarding the older in favor of the newer. This automatic or semiautomatic updating of files between the satellite and the host addresses a long-standing problem. The updating routines may also incorporate a backup option to save older files.

Another useful feature in host/ μ PDA communication is a means for a user to select and compose a mix of executable program files for downloading to a μ PDA, either replacing or supplementing those executable routines already resident. A user can have several different program lists for downloading as a batch, conveniently configuring the applicability of a μ PDA among a wide variety of expected work environments.

Such applications as databases, spreadsheets, documents, travel files such as currency converters, faxing and other communications programs, time clocks, address and telephone records, and the like, may comprise customized lists of user-preferred applications.

In another embodiment, an undocked μ PDA can transfer data via the optional expansion bus 40 (Fig. 3) directly to a host. In the special case of a μ PDA user without access to a PCMCIA interface on his host (notebook or desk-top) computer, he or she can connect to a host via an auxiliary port on the host, such as a serial port, via the expansion bus interface. In this case, the μ PDA still requests password(s) from the host, and controls access to its on-board memory according to the password(s) received.

The optional expansion interface may also be used in some embodiments while a μPDA is mastered by a host, wherein the host may effectively send data through the bus structures of the μPDA .

Additional Aspects and Features

Software Vending Machine:

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In a further aspect of the invention, a Software Vending Machine with a very large electronic storage capacity is provided, wherein a µPDA user may dock a module and purchase and download software routines compatible with the µPDA environment.

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Fig. 8 is an isometric view of such a vending machine 61 having a docking bay 63 for a μPDA, a credit card slot 65, and a paper money slot 67. A display 69 provides a user interface for reviewing and purchasing software from the vending machine, along with selector buttons such as button 71 along the sides of the display. In an alternative embodiment the display may also have a touch screen, and may, in some embodiments, emulate the μPDA I/O area on a larger scale.

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In operation, a user may, in this embodiment, review software for sale simply by docking his µPDA unit in the vending machine and selecting from a menu on display 69. The menu may allow the user to browse all available applications, or list new applications since entered dates. The user can select certain applications, try them out, at least in simulation, and then select applications to purchase.

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The vending machine, once all the requirements are met, such as proper identification and payment, copies the selected application(s) to the memory of the μPDA , or, alternatively, to a floppy disk provided by either the user or the vending machine. In this case there is also a floppy disk drive 73 in the vending machine and a port 75 for dispensing formatted floppies for a customer to use in the disk drive. This mode is useful for the instances where a user's μPDA is loaded beyond capacity to receive the desired software, or the user simply wishes to configure the software mix himself from his or her own host computer.

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There may also be provided a backup option so a user may instruct the vending machine to read and copy all or a selection of his files to one or more floppy disks before installing new files or data.

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As described above, each user's μPDA includes an EEPROM or other storage uniquely identifying the μPDA by a serial number or

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other code(s), so the vending machine may be configured in this embodiment to provide the software in one of several modes.

A user may buy for a very nominal price a demo copy of an application, which does not provide full capability of the application, but will give the user an opportunity to test and become familiar with an application before purchase. Also, the user may buy a version of the same application, configured to the ID key of the μPDA to which it is loaded, and operable only on that μPDA . In another embodiment, the software is transferable between a family of keyed μPDA s, or has the ability to "unlock" only a limited number of times. In these cases, the applications would be sold at a lesser price than an unlocked version. The unlocked version works on any $\mu -PDA$ and/or host/ μPDA system. The higher price for the unlocked version compensates for the likelihood of unauthorized sharing of the vended applications.

The vending machine could also offer a keyed version, customized to operate only on the μPDA docked in the software vending machine, or upon a family of $\mu PDAs$. This keyed version is possible because of the individual and unique nature of each μPDA , which has, at a minimum, a unique serial number, and may also have other security programming, as described above, which allows a vending machine to prepare and download a customized copy of an application that will operate only on the particular module for which it is purchased.

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There are a number of different means by which unique correspondence might be accomplished, as will be apparent to those with skill in the art. A standard version stored in the memory facility of a vending machine might be recompiled, for example, on downloading, using a unique code from the docked or identified μPDA as a key in the compilation, so only the specific μPDA may run the program by using the same unique key to sequence the instructions while running. The key for scrambling or otherwise customizing an application might also comprise other codes and/or

executable code sequences stored uniquely in a µPDA.

In yet another aspect related to the vending machine, there is a printer outlet 77 which prints a hardcopy manual for the user. It is, of course, not necessary that the software vended be specific to the M-PDA. Applications may also be vended for other kinds of machines, and transported in the memory of the μ PDA, or by floppy disk, etc. In this embodiment a non- μ PDA user can acquire a wide assortment of software.

The software vending machine may also serve as an optional informational display center in such locations as airports, train stations, convention centers, and hotels. Upon inserting a µPDA a user may interface directly and upload current information including, but not limited to, local, national, and world news; stock quotes and financial reports; weather; transportation schedules; road maps; language translators; currency exchange applications; E-mail and other direct on-line services.

A customized vending machine could be tailored to business travelers and allow fast access to pertinent information, allowing the user to download files to send via E-mail. In another aspect of the invention, the vending machines are linked to each other allowing users to send messages to associates travelling through locations of associated vending machines. Such dedicated µPDA E-mail is immediately downloaded to a specific µPDA as it is docked. The sender may have the associate's µPDA unique encoded key as identification, or some other dedicated identifying means for E-mail.

In another embodiment, as each business associate arrives at an airport, he or she may prompt the custom vending machine in that location via an optional installed infrared interface (not shown) in their µPDA. The custom vending machine, also equipped for infrared communication, receives the signal and sends/or receives any messages that are waiting.

Enhanced Display:

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Fig. 9 is a plan view of an enhanced I/O interface unit 79 according to an aspect of the present invention. Interface unit 79, with about a 5-inch diagonal measurement, comprises a combination LCD display at least partially overlaid by a touch-sensitive input screen, providing an I/O area 80 in much the same manner as in a μPDA. Four docking bays 81, 83, 85, and 87 are provided in the left and right edges of interface unit 79 in this embodiment, and are configured for PCMCIA type II modules. One of these bays may be used for docking a μPDA according to the present invention, and the other three to provide a larger CPU, additional memory, battery power, peripheral devices such as modems, and the like by docking functional PCMCIA modules.

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Interface unit 79 is a framework for assembling a specialty computer through docking PCMCIA units, including a μ PDA according to the present invention. In other embodiments where the μ PDA assumes other form factors, the docking bays may be configured accordingly.

A docked μPDA in this embodiment is configured to produce its I/O display on I/O area 80. The thumbwheel on the M-PDA is accessible while docked and acts as described above in the stand-alone mode in this case. In another aspect, the enhanced display has a re-configured output that enables the user to manipulate the data from the touch-screen alone and/or additional hardware selector buttons and/or a standard keyboard attached to the enhanced display via a dedicated bus port, or even through the expansion port of a docked μPDA. In a further embodiment the enhanced display has a dedicated mouse port and/or a dedicated thumbwheel.

In yet another embodiment, interface unit 79 has an inexpensive, conventional, replaceable battery and/or a rechargeable battery. Also, in another aspect, interface unit 79 may dock two or more individual µPDAs and cross-reference data files between them according to control routines that can manipulate mutually unlocked files. Further still, interface unit 79 may be placed and structurally

supported for easy viewing on a dedicated standard or smaller-sized keyboard, connecting to the keyboard as an input device. The keyboard would then automatically serve as the input device.

Interface unit 79 for a μPDA is small and compact enough to slip into a pocket book or briefcase, providing a very portable, yet very powerful, computer.

Microphone/Voicenotes:

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Fig. 10 is a plan view of a μPDA 110 with an I/O interface 116, an expansion port 120, and a host interface connector 114. μPDA 110 has all the features previously described and additionally a microphone 88. In this embodiment, control routines in the μPDA use a linear predictive coding (LPC) approach to convert analog input from the microphone to a digital voice recording. This approach uses a minimum of memory, but still is capable of reproducing audio input like the human voice within recognizable limits.

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In an alternative embodiment, for better quality voice recording, a two-step integrator may be used in order to separate the analog signal and synthesize a closer digital representation.

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With a μ PDA so configured, a user's voice notes can be recorded and later uploaded to a host for processing. In future embodiments the digital signals may be converted to text or sent as voicemail on a network. In yet another embodiment, the microphone is integrated with a speaker for editing purposes.

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Cellular Telephone Interface:

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Fig. 11 is an isometric view of a μ PDA 10 docked in a dedicated cellular telephone 45 according to an embodiment of the present invention. Telephone 45 has a docking port 49 for a μ PDA according to the invention. In this embodiment, port 49 is on one side of telephone 45, and there is a window 51 to provide access to I/O

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interface 16 of the μPDA after it is docked. With the μPDA docked, all of the software and memory of the μPDA is available to the telephone and a user may operate the phone by I/O interface 16.

In this aspect of the invention, unique control routines and display configurations are provided to enhance use of the cellular phone. For example, all of the user's collection of phone numbers, associated credit card numbers, access codes, etc. are readily available and may be quickly and conveniently accessed and used. In one aspect, a simple input displays alphabet letters to select, and once a letter is selected, a partial list of parties that might be called is displayed. One may scroll through the list by touch input or by use of the thumbwheel of the μPDA and select a highlighted entry. It is not required that the telephone numbers be displayed.

Once a party to be called is selected, the μPDA dials the call, including necessary credit card information stored in the memory of the μPDA for this purpose.

In a further embodiment, the calls are timed and time-stamped and a comprehensive log, with areas for notes during and after, is recorded.

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In another embodiment, conversations are digitally recorded and filed for processing later. A future embodiment may include a voice compression program at a host or within cellular phone 45. Compressed voice files, such as, for example, messages to be distributed in a voicemail system, may be downloaded into the μ PDA or carried in a larger memory format inside the cellular telephone. The μ PDA can then send the files via a host or dedicated modem attached at connector portion 20 to the optional expansion bus 40 (Fig. 6).

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The cellular telephone may, in this particular embodiment, have a bus port for digital transmission. In this case, the compression algorithm along with voice system control routines are also established at the receiving end of the transmission to uncompress the signal and distribute individual messages.

In a further embodiment, voice messages may be sent in a wireless format from the cellular telephone in uncompressed digital synthesized form, distributing them automatically to dedicated receiving hosts, or semi-automatically by manually prompting individual voicemail systems before each individual message. In a further aspect of wireless transmission, a microphone/voicenote μ PDA as in Fig. 10 may send previously stored voicenotes after docking in a cellular telephone interface.

In Europe and Asia a phone system is in use known as CT2, operating on a digital standard and comprising local substations where a party with a compatible cellular phone may access the station simply by being within the active area of the substation. In one aspect of the present invention, a CT2 telephone is provided with a docking bay for a μ PDA, and configured to work with the μ PDA. In yet another aspect of the invention, in the CT2 telephone system, and applicable to other digital telephone systems, a compression utility as disclosed above is provided to digitally compress messages before transmission on the CT2 telephone system.

It is roughly estimated that a dedicated compression algorithm may compress ten minutes of voice messages into one minute using the existing CT2 technology. This would save on telephone use charges significantly. In this aspect, there needs be a compatible decompression facility at the receiving station, preferably incorporated into a standard μ PDA voicemail system for CT2 or other digital transmissions.

In a further embodiment, control routines are provided to enable the microphone/voicenote μPDA as illustrated in Fig. 10 to carry digital voicenotes, either compressed or uncompressed. When docked in a CT2-compatible μPDA cellular telephone, the μPDA in this embodiment can transmit the digital voicenotes in compressed form.

Speaker/Pager:

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Fig. 12 is a plan view of a μPDA 210 with a microphone/speaker area 90 and a pager interface 92 according to an embodiment of the present invention. This μPDA has the ability to act as a standard pager, picking up pager signals with installed pager interface 92 and alerting a user through microphone/speaker 90. Once the signals are received, μPDA 210 can be docked in a compatible cellular telephone as illustrated in Fig. 11 and the μPDA will automatically dial the caller's telephone number. All other aspects are as described in the docked mode in the cellular telephone.

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In another embodiment, the speaker/pager μPDA can be prompted to generate DTMF tones. The DTMF tones are generated from a caller's telephone number.

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The speaker/pager μ PDA can store pager requests in its onboard memory. It can also display all pager requests including time and date stamps, identification of the caller, if known, and other related information, on I/O interface 216. In this particular embodiment, a user can receive a page, respond immediately in digital voicenotes on the μ PDA via speaker/microphone 90, and then send the response from a dedicated μ PDA-compatible cellular telephone or conventional telephone.

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Wireless Infrared Interface:

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Fig. 13 is a plan view of a μPDA 310 with an IR interface 94 according to an embodiment of the present invention. In this embodiment the μPDA may communicate with an array of conventional appliances in the home or office for providing remote control. Unique signals for the appliances are programmed into the μPDA in a learning/receive mode, and filed with user password protection. Once a correct password in entered, an icon-based menu is displayed on I/O area 316 in a user-friendly format. A master routine first queries a user for which device to access. For example, in a residential application, icons are displayed for such things as overhead

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garage doors, security systems, automatic gates, VCRs, television, and stereos.

In another aspect of the invention, a receiving station such as a host computer or peripheral interface has IR capabilities to communicate data directly from a nearby μPDA with an infrared interface. In a further embodiment the μPDA may interface in a cellular network and act as a wireless modem.

PERIPHERALS

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A μ PDA may serve as the platform for various peripheral attachments via expansion port 20 (Fig. 1B and others). Upon attachment to a peripheral, a dedicated pin or pins within expansion port 20 signal microcontroller 11, and a peripheral boot-strap application is executed. Interfacing control routines, which may reside in the peripheral or in the memory of the μ PDA, are then executed, and the μ PDA I/O interface displays the related menu-driven options after the linking is complete.

Scanner:

Fig. 14 is a plan view of a μPDA 10 with a scanner attachment 55 according to an embodiment of the present invention. The scanner attachment is assembled to the μPDA, making electrical connection via expansion port 20. In this embodiment the physical interface of the scanner is shaped to securely attach to the μPDA. Scanner attachment 55 has a roller wheel 57 or other translation sensor, which interfaces with wheel 18 of the μPDA, providing translation sensing in operation for the resulting hand-held scanner. In another aspect, scanner attachment 55 has a translation device which transmits the proper signal through expansion port 20. The scanner bar is on the underside, and one or more batteries 59 are provided within the scanner attachment to provide the extra power needed for

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light generation.

In the scanner aspect of the invention, scanner attachments 55 of different width D2 may be provided for different purposes. The bar may be no wider than the μ PDA, or may be eight inches or more in width to scan the full width of U.S. letter size documents, or documents on international A4 paper. Unique control routines display operating information on the μ PDA's I/O area 16 for scanning, providing a user interface for setup of various options, such as the width of the scanner bar, and providing identification for files created in the μ PDA memory as a result of scan passes. Scanned data stored in the μ PDA memory may be quickly transferred to the host via host interface 14 when the μ PDA is docked. Unique routines may be provided to automate the process, so the user does not have to search for files and initiate all of the transfer processes.

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Facsimile Option:

Fig. 15 is a plan view of a μPDA with a fax-modem module 89 attached according to an embodiment of the present invention. A fax and telecommunication capability is provided via conventional telephone lines to the μPDA by fax-modem 89 interfacing to expansion bus interface 20. The fax-modem has internal circuitry for translating from the bus states of the expansion bus to the fax protocol, and a phone plug interface 91. In another aspect, the μPDA can be docked in a host and be used in combination with fax-modem 89 to provide faxing and file transfers of both host and μPDA data files. In this case, the fax-modem routines are displayed on the host monitor.

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Printer:

Fig. 16 is a plan view of a μPDA with a Centronics adapter interface according to an embodiment of the present invention. A

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printer connector 93 engages expansion interface 20 by a connector 95 through a cable 97. Translation capability resides in circuitry in connector 93, which is configured physically as a Centronics connector to engage a standard port on a printer.

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Barcode Reader and Data Acquisition Peripheral:

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Fig. 17 is an isometric view of a μPDA 10 docked in a barcode reader and acquisition peripheral 100 according to an embodiment of the present invention. μPDA 10 is docked in docking bay 149. I/O interface 16 displays information through opening 147 according to specialized data acquisition applications. In this particular embodiment peripheral 100 has an IR interface 94, a microphone 103, a scanner port 101 (not shown), battery pack 105, and a numeric keypad pad 96 implemented as a touch-sensitive array.

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Application routines enable the data acquisition peripheral to operate as, for example, a mobile inventory management device. The user may scan barcode labels with scanner 101 and enter information, such as counts, on keypad 96 or by voice input via microphone 103. Since applications of peripheral 100 are very specialized, only a limited voice recognition system is needed. The voice recognition system may prompt other command routines within the master applications as well.

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As inventories are collected, the database may be displayed and also manipulated directly via I/O area 16 in open bay 147, or information may be downloaded at a prompt to a nearby host via IR interface 94.

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Alternatively to frequent data transmission, data may be stored or an auxiliary option memory location in peripheral 100.

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In another aspect, the data acquisition peripheral may be interfaced to the analog output of a monitoring device, such as a strip chart recorder, and may digitize and store the incoming analog signals.

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Solar Charger:

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Fig. 18 is an isometric view of the side of a μPDA 10 opposite the I/O interface with a solar charger panel 98 according to an embodiment of the present invention. Panel 98 is positioned so that when μPDA 10 is in strong light, such as sunlight, the solar charger absorbs the solar energy and converts it to electricity to recharger battery 15 inside the μPDA. Solar charger 98 may be permanently wired to the circuitry of the μPDA or attached by other means and connected to a dedicated electrical port or the expansion port. The solar charger is placed so that the μPDA can be fully docked in a docking port with the panel in place. In another aspect, a detachable solar charger may be unplugged before docking the μPDA,

and the detachable charger may then be of a larger surface area.

Games/Conference Center:

Fig. 19 is a largely diagrammatic representation of a Games Center unit 33 according to an aspect of the invention for connecting several μPDA units (37, 39, 41, and 43) together to allow competitive and interactive games by more than one μPDA user. Games Center unit 33 is controlled by an 80486 CPU in this particular embodiment. μPDAs may be connected to the central unit by cable connection via the expansion bus or the host interface of each μPDA, through a connector such as connector 35. The drawing shows four connectors, but there could be as few as two, and any convenient number greater than two.

As a further aspect of the present invention, the gaming center may serve as a conference center where a number of $\mu PDAs$ may exchange information. In this way, for example through custom routines stored and executable in central unit 33, a manager may update a number of salespeoples' $\mu PDAs$, including but not limited to merchandise databases, spreadsheets, price sheets, work assignments,

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customer profiles, address books, telephone books, travel itineraries, and other related business information while in conference.

Standard Keyboard:

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Fig. 20 is an isometric view of a keyboard 151 connected by a cord and connector 153 to a μPDA 10 via the expansion port 20. In this example, the keyboard is a mechanical keyboard having a full-size standard key array and an on-board controller and interface for communicating with the μPDA. In other embodiments the keyboard may take many other forms, including a two-layer, flexible, roll-up keyboard as taught in U.S. Patent 5,220,521.

In addition to keyboards, other input devices, such as writing tablets and the like may also be interfaced to a μPDA via expansion port 20.

There are numerous additional ways to combine different embodiments of the μPDA for useful functions. For example, an IR-equipped μPDA attached to scanner 55 may transfer large graphic files in near real time to a host computer. If the files were of text, the host may further process the files automatically through an optical character recognition (OCR) application and send the greatly reduced ASCI files back to the μPDA . As discussed above, the μPDA family of devices establishes a protocol of software security and distribution as well as having the ability to be bus mastered by a host computer system for numerous applications.

A Flexible, Roll-Up Keyboard

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As computer systems have become smaller and smaller, one of the difficulties has been the ability to provide adequate input apparatus, such as keyboards, for use with the small, portable systems.

Fig. 20 above and the accompanying text to Fig. 20 describes a full sized keyboard for use with a μPDA according to the present

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invention. Such a keyboard allows a user to provide keyed-in input to the memory of a μ PDA, but still has a disadvantage in that the keyboard is less portable than the μ PDA itself. In one aspect of the present invention, a flexible keyboard is provided which may be rolled up for transport and storage, and which, in one embodiment, is also cordless, communicating with a μ PDA by code carried on a dynamic magnetic field. Such a keyboard and inductive coupling technology for cordless communication is described below.

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Fig. 21A is a plan view of a flexible, roll-up keyboard 1011 according to the present invention. Fig. 21B is an elevation view of Fig. 21A. In the embodiment shown by Fig. 21A, the keyboard has a layout according to the popular "IBM advanced" format. The flexible keyboard of the invention can be implemented in any convenient format. In this embodiment the dimensions are about 50 cm. long and 20 cm. wide.

The keyboard is formed by two molded layers of flexible material, such as polyurethane, although there are a number of other suitable materials, among them natural rubber and several types of synthetic rubber. An upper layer 1013 has shaped pads for keys, and a lower layer 1015 has electrically conductive traces forming circuitry for communicating keystrokes to control circuit module 1017 located in a pocket between the layers. A battery may also share the pocket in some embodiments, or be carried in a similar pocket elsewhere between the layers.

The two flexible layers fasten together in the embodiment of Fig. 21A and Fig. 21B by flexible appendages from one layer "snapping" into receptacles in the other, which provides for relative movement between the layers when the keyboard is rolled up for transport or storage. Details of a key cell, traces, layer attachment, and so forth are provided further below.

Fig. 21C shows flexible keyboard 1011 of Fig. 21A rolled into a compact cylinder and secured by a band 1019. In this view the

band is a simple plastic cylinder, but could be any of a number of alternative devices, such as rubber bands, cord, Velcro loops, and the like. There are also several ways that fasteners may be attached to the keyboard to keep the keyboard rolled up, such as Velcro pads attached by adhesive.

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Fig. 22 is an isometric view of a single key cell 1021 in the flexible keyboard according to the embodiment of Figs. 21A and 1B. The key cell is based on a dome shape 1023 molded into upper layer 1013, which provides for necessary vertical movement to accomplish a contact closure for a keystroke and to prevent closure otherwise. The molded dome shape includes a finger pad 1025 molded at the top of the dome. The finger pad may have a concave upper surface with letter or character symbols, such as the letter "A" shown in Fig. 22, applied to the upper surface. The letters or characters may be silkscreened, printed, or applied by a number of other techniques known in the art. The letters and characters may alternatively be molded into the surface as depressions below the level of the finger pad surface, so repeated use is less likely to wear the letters and characters away. Additionally, there may also be "breather" openings 1027 through the sidewall of the dome structure to allow air under the dome to escape when a keystroke is made. When a user presses down on the dome, the dome collapses, and when the pressure is removed. the dome returns to its original shape. That is, the "normal" position of the dome is in the uncollapsed condition.

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Fig. 23A is a section view of the key cell of Fig. 22 taken along line 23A-23A. Lower layer 1015 and upper layer 1013 are joined by a fastening technique (not shown) in a manner to position each dome structure in upper layer 1013 over the corresponding conductive structure of lower layer 1015. The fastening technique is described in detail below.

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Lower layer 1015, as described briefly above, has electrically conductive traces for signalling keystrokes to control circuit module

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1017, for encoding the signals to be transmitted to a computer. Fig. 23B is a plan view of layer 1015 at the key cell of Fig. 23A in the direction of arrow 1029. Dotted circle 1031 represents the outer periphery of dome structure 1023 of upper layer 1013.

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In about the center of the dome structure area, lower layer 1015 has a raised portion 1033, also seen in elevation in Fig. 23A. The upper surface of raised portion 1033 is electrically conductive in regions 1035 and 1037, but not in central region 1039. An electrically conductive trace 1041 connects to conductive region 1035, and another electrically conductive trace 1043 connects to conductive region 1037. Raising portion 1033 above the level of the surface of layer 1015 is primarily a convenience rather than a requirement. Also, there are a number of ways conductivity might be imparted to the conductive regions. For example, the traces could be formed by coating the entire surface with conductive material, then removing material to shape the traces. Conductive material could be molded

into the flexible material, as well, and there are other ways.

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Referring to Fig. 23A, there is a contact closure extender portion 1045 on the lower surface of finger pad 1025 that is similar to raised portion 1033 on lower layer 1015, and positioned directly above portion 1033 when layers 1013 and 1015 are joined. The underside of extender portion 1045 is also electrically conductive, and forms a contact closure element for selectively providing electrical closure between traces 1041 and 1043 when a user depresses dome 1023 by finger pressure. Contact closure causes a signal to be transmitted to control circuit module 1017 to be coded for transmission. The traces and other regions of conductive material can be formed on the flexible surfaces of the molded layers in a number of different ways. In the embodiment described here, these conductive regions may be provided by a conductive, rubber-based coating applied in a conventional manner, such as by silkscreening, and other masking techniques.

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It will be apparent to one skilled in the art that there are many

variations that could be used for the sidewalls of the dome for an individual key cell. Wall thickness and angle can be varied, for example, to vary the force required to perform a keystroke, which affects the "feel" of the key to a user. Similarly, the key cell can be designed with different distances between raised portion 1033 and extended portion 1045. As shown in Fig. 22, there may be vents 1027 in dome 1023 to allow air to escape from the key cell when it is depressed. The feel of a keystroke can also be varied by varying the number or size of vents 1027.

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It is apparent in the plan view Fig. 21A, that there are several different sizes and shapes of key cells in the pattern of the keyboard that are not of the size and shape of the key cell shown in Figs. 22, 23A, and 23B, but larger. The Enter key is an example. In these cases, substantially all of the upper surface is a finger pad, and the wall and upper sections are designed to provide a similar resistance to manipulation as a smaller key cell. In the case of these larger key cells, more extensive contact closure patterns are provided on raised portions of lower layer 1015 than the simple pattern shown in Fig. 23B, and corresponding contact closure pads are provided so a keystroke will be accomplished regardless of where on the fingerpad a user depresses the fingerpad.

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Fig. 24A is a plan view of lower layer 1015 at the position of the Enter key to illustrate this principle. Upper layer 1013 is removed in this view. In Fig. 24A, dotted line 1047 outlines the region of the Enter key on the keyboard of Fig. 21A. Raised portion 1049 is shaped to cover the central area enclosed by the extended key cell. Regions 1051 and 1053 are electrically conductive, and region 1055 between the conductive regions is non-conductive. Trace 1057 connects to region 1053 and trace 2059 connects to region 1051.

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Fig. 24B is a plan view of upper layer 1013 from the same vantage as for Fig. 24A. Line 1061 is the outline of the dome area for the Enter key, and line 1063 is the outline of the fingerpad area

for the Enter key. Dotted outline 1065 is the outline for a contact closure extension on the bottom surface of upper layer 1013 matching the shape of raised portion 1049 of Fig. 24A. The underside of the contact closure extension is electrically conductive as described for the single key cell of Fig. 23A. This arrangement for the contact closure structure allows larger areas to function similarly to the smaller areas.

It will be apparent to one with skill in the art that a long, straight contact closure structure similar to that described for the Enter key would be useful for the space bar, and other shapes could be provided for other keys.

The keyboard of the present invention as thus far described is what is known in the art as a "hard contact" keyboard, which simply means that the keystrokes are contact closures. The "wiring" provided by the electrically conductive traces described above amounts to a conventional matrix circuit generally used in the art for this kind of keyboard. The matrix circuit may be connected to an on-board microprocessor which "reads" the contact closures and codes the information for transmission to a computer. Similarly, the matrix circuit could interface with a multi-lead cable for connection to a computer, and the controller for converting key strokes to digital data for the computers data bus could be in the computer itself, rather than at the keyboard.

Fig. 25A is an isometric view of the end of the flexible keyboard where the control circuitry can be housed, showing layers 1013 and 1015 separated to illustrate internal details. Lower layer 1015 has a pocket 1067 for positioning and retaining control circuit module 1017, and upper layer 1013 has a matching pocket 1069, such that when the layers are joined, the control circuit module is enclosed and retained.

Electrically conductive traces 1071 comprise the traces described above associated with the individual key cells, arranged in a conventional matrix for signalling contact closures of individual keys

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and key combinations. The traces are formed on the surface of lower layer 1015 and wrap over the edge of pocket 1067, and down the side of the pocket, forming contact pads for interconnecting with control circuit module 1017.

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Fig. 25B shows control circuit module 1017 at an angle of about 90 degrees from the view of Fig. 25A. Control circuit module 1017 is shown as an encapsulated unit comprising a microprocessor and associated circuitry for monitoring key activity, encoding the activity, and transmitting the encoded data an associated computer. Encapsulated with module 1017 are contact pads 1073 formed of metallic conductor material, such as wire or strip. Module 1017 comprises the conventional control circuitry commonly used in the art for coding keystrokes, as communicated by the conventional wiring matrix to the key closures, for transmission to an associated computer.

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Fig. 25C is a view of module 1017 in the direction of arrow 1075 to illustrate the arrangement of contact pads 1073 relative to module 1017. Contacts 1073 are formed to extend from module 1017 to one side, so when module 1017 is inserted in pocket 1067, contact pads 1073 urge against traces 1071 on the side of the pocket, establishing electrical contact between pads 1073 and traces 1071. The arrangement shown allows relative movement between components when the flexible keyboard is rolled for transport or storage, without damage to conductors or other components.

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Keyboard 1011 of the present invention has been described above as having two layers, which is preferred by the inventor to facilitate matrix wiring and other details of the invention. It has been found that some relative movement between the layers is useful when rolling the keyboard as shown in Fig. 21C. Accordingly, keyboard layers 1013 and 1015 are connected by providing receptacle openings in one layer and connector extensions in the other. Extension 1075 from lower layer 1015 and opening 1077 in upper layer 1013 as shown in Fig. 25A are exemplary of such connection means.

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Fig. 25D is a section view taken along line 25D-25D of Fig. 25A with layer 1013 and 1015 joined. Bore 1077 is formed in the shape of an inverted, truncated cone when layer 1013 is molded, and extension 1075 is formed in a similar shape when layer 1015 is molded. To join the two layers, the molded extensions are forced into the openings. When the keyboard is subsequently rolled, the extensions flex, allowing limited relative movement between the layers.

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It will be apparent to one skilled in the art that there are other, essentially equivalent ways to join the two layers. For example, openings could be provided in both layers, and a separate flexible connector inserted into matching openings to hold the two layers together. There are similarly many different shapes that might be used for the extensions or connectors and the openings.

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To serve as an input device for a computer it is necessary that the keystrokes be communicated to the computer. In one embodiment, as described above, keyboard 1011 of the present invention is provided with a controller for transmitting codes to the computer indicating the keystrokes and combinations of keys depressed. In the embodiment of Fig. 25A a connector receptacle 1079 is formed in layer 1015 when the layer is molded. A connector cable 1081 is provided with an end 1083 configured to nest in receptacle 1079.

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Conductive traces 1085 similar to the matrix traces described above are applied to lower layer 1015 between pocket 1067 and receptacle 1079, and along the vertical walls of both openings.

Module 1017 has contact pads (not shown) on the end facing receptacle 1079, similar to the contact pads 1073 illustrated in Fig. 25B and Fig. 25C. Similarly, cable end 1083 has contact pads 1087 for contacting the traces in receptacle 1079. The other end of cable 1081 has a connector to match the computer receptacle, such as an AMP connector.

Fig. 26A shows an alternative embodiment that communicates with a suitably equipped computer without a cable. Layers 1013 and 1015 have pockets 1089 and 1091 respectively for positioning and retaining a battery 1093 which connects to a control module 1101 through traces 1098 between the pockets, similar to traces described above for other connections. The battery is made to be about the width and height of the control module to facilitate rolling of the keyboard.

At the other end of pocket 1067 for control module 1101 there is a pocket 1095 in lower layer 1015 and another pocket 1097 in upper layer 1013 to retain a coreless encapsulated magnetic coil 1099, which is driven by output signals from control module 1101 through connecting traces 1098 as described above. In this embodiment the coil is driven at a controlled frequency, and data to be transmitted to a computer is coded into the changing magnetic field produced by the coil. A sensing coil in a computer senses the coded data, decodes it, and puts it on the computer bus as digital data. Further detail relative to inductive coupling is provided below.

Another means of transmission of data from keyboard 1011 to an associated computer is coded light transmission. In this embodiment, a light transmitter replaces coil 1099, and control module 1101 drives the transmitter to send coded signals to a receiver at the computer. Coded light transmission is known in the art for sending data from a keyboard to a computer.

Fig. 26B shows yet another alternative embodiment wherein there is no local control module. In this embodiment conductive traces 1071 lead to a pocket 1102 formed in layer 1015, and a connector 1100 and a cable 1104 have a number of conductors equal to the number of conductive traces. The connector resides in pocket 1102, and the key strokes are communicated through the cable to the computer (not shown). A controller in the computer, not a part of the keyboard, translates the keystrokes, digitizes the data, and places the

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data on the computer bus. This embodiment has the disadvantage of requiring a cable with many more traces than the embodiments with an on-board controller, but the absence of the controller in the keyboard is an advantage.

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Fig. 27A shows a flexible keyboard 2103 according to the present invention connected to a computer 1105 by a transmission cable 1081 as described above. The computer may be of many types, including the popular desktop models. Computers that use keyboards typically include a CPU, an electronic storage system such as a hard disk drive, connected to the CPU and other devices by a bus system, and may also have a display terminal.

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Fig. 27B shows a flexible keyboard 1107 according to an embodiment of the invention wherein code is transmitted by a magnetic field, coupled with a computer 1109. The computer in this case has a coil for sensing the magnetic field 1111 produced by the transmission system of the keyboard, and a demodulation system (not shown) for decoding the data transmitted and putting it on the digital bus of the computer for use by the CPU. Further detail regarding inductive coupling is provided below, with examples of a rigid keyboard, although the coupling apparatus and technique is applicable to a flexible keyboard as well.

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It will be apparent to one with skill in the art that there are many changes that might be made to the embodiments described without departing from the spirit and scope of the invention. A number of differences within the scope of the invention are described above. There are many materials that would be suitable, for example, and a number of different ways that key cells might be formed. There are also many different ways traces may be routed, many different kinds of material for forming traces, and different places that pockets might be located for components of the keyboard. Such differences should be considered as within the spirit and scope of the invention.

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Inductive Coupling

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Fig. 28 is an isometric view of a general-purpose portable computer with a keyboard coupled by induction apparatus according to the present invention. The computer system of Fig. 28 comprises a principle unit 2013 including a housing 2017 and a tilt-up display 2019. The display may one of several types of available flat-panel displays. Housing 2017 contains the principal electronic operating elements, such as a CPU microprocessor, an I/O bus, and system Ram memory. There are connectors (not shown) for connecting to serial devices and the like, which is typical for portable computers.

A separate keyboard unit 2015 provides alphanumeric and other keystroke input without benefit of a cable connection, by means of encoded information transmitted on a magnetic field represented by magnetic lines of force 2021.

Fig. 29 is a schematic illustrating some of the internal elements of keyboard 2015. In this particular embodiment the key switches (hereinafter the keys) are connected in a matrix 2023 and scanned by a microprocessor 2025 contained within the framework of the keyboard. The scanned matrix arrangement greatly reduces the number of I/O port bits needed for large numbers of keys. This scanned matrix arrangement is but one of a number of convention means by which the key operations may be recognized by an on-board microprocessor, and is used here as an example. The invention may actually be combined with virtually any means known in the art for recognizing and coding the keystrokes. Further, there are a number of other conventional features about keyboards that are not covered here because they have little bearing on the invention. For example, keyboards typically have a small number of indicative LED's for indicating Num Lock, Caps Lock, Scroll Lock, etc., and these features are not described herein.

The scanned matrix of Fig. 29 shows 16 keys interfaced to

microprocessor 2025 through a four-bit input port 2029 and a four-bit output port 2031. Pull up resistors (resistor 2033 is an example), connected to an on-board, battery-driven power supply (the connections are not shown in Fig. 29), provide a high level at the input port when no keys a pressed in a column.

In scanning, the matrix is read one row at a time. One of the output port bits is set to 0 while all the others are set to 1. In Fig. 29 the second row down is being read. The keys in rows with the output bit set to 1 are effectively disabled, as pressing a key would only connect a high-level output port bit to an input port bit that is already pulled high by the pull-up resistor. The keys in the row driven by the 0 set bit are active. Pressing any key in the active row pulls the input port bit in the corresponding column low. Each row is read in turn, and the sequence is repeated indefinitely.

The matrix must be fully scanned at a rate to insure if a user quickly presses and releases a key, the contact will not be missed by the microprocessor. In fact, not only the making of a contact is recorded, but breaking as well, which allows for key combinations to be recorded. It will be apparent to those with skill in the art that with the schematic shown, key combinations may result in "ghost" key readings, but this potential problem is handled by placing a diode (not shown) in series with each key.

The 16 key matrix is provided as an example, and is extensible in a number of ways to more keys. For example, input and output ports with more bits may be used. One 16 bit input and one 16-bit output port will support a matrix of 256 keys. Alternatively more ports with four or eight bits may be used. Typically a complete keyboard scan is made once every few milliseconds.

In a conventional keyboard and in the present embodiment of the invention the on-board microprocessor converts the information derived from scanning to hexadecimal scan codes. Many sorts of scan codes may be used, and the invention is not limited by the selection of

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scan codes to be used.

In a conventional system, the microprocessor sends the scan codes serially over a four-wire cable, with one wire carrying all of the data. The main portion of the computer in the conventional case receives these scan codes at a special I/O port, where a keyboard controller chip issues an interrupt to the CPU that a scan code is available to be read. The CPU then reads the scan code and interprets the key stroke information as input. The program for doing so is typically a part of the system BIOS.

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In the system of the present invention there is no cable to transmit scan codes to the computer. In the embodiment shown by Fig. 29, microprocessor 2025 operates a current controller 2035 controlling current in a circuit 2037 from a power supply 2039. The controlled current passes through a one-turn generator loop 2041, creating a magnetic field represented by exemplary field lines 2043. In Fig. 29 generating loop 2041 is shown in plan view, so magnetic field lines 2043, which are generated orthogonal to the plane of the loop and conventionally circular in nature, are shown as lines.

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Fig. 30 shows generating loop 2041 of Fig. 29 and a receiving unit 2045 located in housing 2017. Receiving unit 2045 provides the function of the special I/O port that receives scan codes by serial cable for a conventional system, and forwards the codes to the system CPU. In one embodiment receiving unit 2045 is implemented as an add-in card for a general-purpose computer, but in other embodiments, the receiving unit is a hard-wired unit in the computer housing. The latter is the preferred case for notebook, laptop, and other computers meant to be highly portable.

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In the present invention information about keystrokes is transmitted to the system CPU via encoded magnetic fluctuations in field 2043. Toward this end unit 2045 has a receiving loop 2047 for intercepting the magnetic field produced by loop 2041. As is well known in field technology, there will be no effect in loop 2047 unless

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there is a change in the strength of the magnetic field. To transmit information, then, it is necessary that the magnetic field be either expanding or collapsing.

As is known in the art of electrical engineering, there exists a defined property between two electrical circuits called mutual inductance. The mutual inductance determines the emf induced in one circuit as a consequence of a change in current in another. The direction of the emf in the one is, of course, a property of the current direction in the other.

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In the embodiment of the present invention shown by Figs. 29 and 30, a current i produced in loop 2041 produces an emf V₁ in loop 2047 by virtue of the change in magnetic field 2043 produced by the change in the current in loop 2047 from 0 to i (and from i to 0). To produce a detectable emf change across the ends of loop 2047, current controller 2035, driven by microprocessor 2025 (Fig. 29) creates a finite burst of current in loop 2041. As this current goes from 0 to i, a positive emf is produced in loop 2047, and as the current in loop 41 drops back to 0, the emf magnitude in loop 2047 changes to a negative value. The change in emf in the invention affords the ability to transmit scan codes.

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It is empirically known and reflected in the concept of mutual inductance that the magnitude of the emf produced in one circuit by a current change in another is proportional to the rate of change of current with respect to time in the one, that is di/dt. Accordingly, the cross section of the transmitting loop is relatively large so as to not present an impediment of high resistance to a rapid rise of current as a result of imposing a voltage on the transmitting loop. In the present embodiment of the invention the transmitting loop is made of square conductor about 2 millimeters on a side and with a mean diameter of about 25 millimeters.

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In the embodiment shown, the current for the transmitting loop and for other operations of the keyboard is provided by battery-based

power supply 2039. It is desirable therefore that the power requirements for the keyboard be kept to a minimum level.

Nevertheless, it is also desirable to operate in a manner to avoid corruption of transmissions with spurious signals caused by ambient magnetic fields, due to current levels in other circuitry of the keyboard, the computer elements in housing 2017, or other equipment that may be in the vicinity of the computer system according to the invention. In the described embodiment current in the transmitting loop is limited to several milliamps, and interference is handled at the receiving unit rather than by higher power requirements for the transmitter.

The induced emf in receiving loop 2047 is monitored by sensing and demodulation circuitry 2049 in the receiving unit according to control routines implemented as part of the system BIOS preferably, and an output register 2051 is set up with received scan codes transmitted from the keyboard. As in a conventional demodulator, when a scan code is "ready" unit 2049 issues an interrupt via I/O bus 2053 to system CPU 2055 that a scan code is available to be read. After a received scan code is read by the system CPU, the output register is reset for the next scan code received.

Fig. 31A is an exemplary trace of a received emf waveform 2057 (V₁ across the ends of loop 2047) as a result of managing i in transmitting coil 2041 according to a pre-programmed code protocol. Fig. 31B shows a serial bit pattern 2058 developed as a result of monitoring the received emf waveform.

In the transmission protocol of the embodiment exemplified by Figs. 31A and 31B, as in a typical serial protocol, there is a preprogrammed bit time, (bt) 2063 which is the reciprocal of the transmission rate (sometimes called the Baud rate). This bit time for scan code transmission need not tax the clock rate of most systems. For example, a very modest ten megahertz clock rate has a period of 10⁻⁷ seconds. Assuming, for example, a bit time of 1000 times the

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clock period and twelve bit times minimum to transmit one scan code, the code may be transmitted in 1.2×10^{-3} seconds. Few people type that fast.

In the present example, the protocol consists of emf spikes across the ends of the receiving loop produced by current bursts in the transmitting loop. Keyboard microprocessor 2025 (Fig. 29) reads the keystrokes, both make and break and key combinations, and sends the scan codes in serial bursts timed according to the bit time. Each current burst in loop 2041 produces a spike such as spike 2059 in loop 2047 which spans about one-fourth of a bit time (this time may vary widely within the bit time).

The scan codes in the present embodiment are the conventional scan codes for U.S. IBM compatible keyboards as referenced above, but there are many alternative codes that might be used in other embodiments.

In Fig. 31 a code start signal is provided by three spikes 2061 timed at twice the bit time rate. Circuitry 2049 (Fig. 30) recognizes the code start signal and monitors the next eight bit times for spikes as indicative of data bits, setting an output register 2051 according to the bits sensed. A bit time with a definite spike is a logical one, and a bit time with no spike produces a logical zero. After the eight bits are transmitted, the transmitter issues a stop code 2065 of five spikes at twice the bit rate, and looks for another start signal. There are a wide variety of start and stop codes (signals) that might be employed in different coding schemes within the scope of the invention, and the means described is but one of the many.

The scan code transmitted in the example of Fig. 31, 00101101, is hex 2D, which is the make code for the "x" key according to the protocol used in the preferred embodiment, which is the conventional scan codes used by most IBM compatible computer keyboard systems. As a user types on the keyboard the system according to the invention continues to interpret the key makes and

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breaks, and the key combinations used, and sends the scan codes to the computer via the transmission loop and the receiving loop.

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It will be apparent to one with skill in the art that there are many alterations that may be made in the embodiments described relative to inductive coupling without departing from the spirit and scope of the invention. A number have been mentioned above. There are many more. For example, there is a broad latitude in the dimensions of the transmitting and receiving loops, and in other variables, such as the current generated and the timing of coded data. Similarly, there are many coding schemes that may be used other than the scheme described in the preferred embodiment. There are many other variations within the spirit and scope of the invention.

It will be apparent to one with the skill in the art that there are many changes that might be made and many other combinations that might be made without departing from the spirit and scope of the invention relative to μ PDAs. There are, for example, many ways to implement the support structure of the μ PDA, and to interconnect the active components. One way has been illustrated by Fig. 2 and described in accompanying text. There are many alternatives to this preferred structure. There is also a broad range of sizes and form factors that might be assumed by devices according to the present invention. The use of well-known PCMCIA form factors has been disclosed, but other sizes and forms might also be provided in alternative embodiments. In larger embodiments, on-board peripherals may be implemented.

In addition to these alternatives, there are various ways the connectivity of a µPDA bus might be provided. The well-known PCMCIA standard has been disclosed as a preference, but other connectivity may also be used in alternative embodiments. Memory types and sizes may vary. Means of providing a security code may vary. The nature of the internal bus may vary. There are indeed many variations that do not depart from the spirit and scope of the

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invention.

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What is claimed is:

1. A personal digital assistant system, comprising:

an enclosure for enclosing and supporting internal elements; a microcontroller within the enclosure for performing digital

module:

a memory means connected to the microcontroller by a memory bus structure for storing data and executable routines;

operations to manage functions of the personal digital assistant

a power supply means within the enclosure for supplying power to functional elements of the personal digital assistant module;

a display means operable by the microcontroller and implemented on a surface of the enclosure;

a host interface means comprising a host interface bus structure connected to the microcontroller and to a first portion of a host interface connecter at a surface of the enclosure, the host interface means configured to directly connect the microcontroller to a compatible bus structure of a host computer;

a flexible keyboard of two connected layers, rollable into a cylinder for storage and transport, having an on-board power supply and circuitry configured to generate a variable magnetic field for broadcasting keystroke codes; and

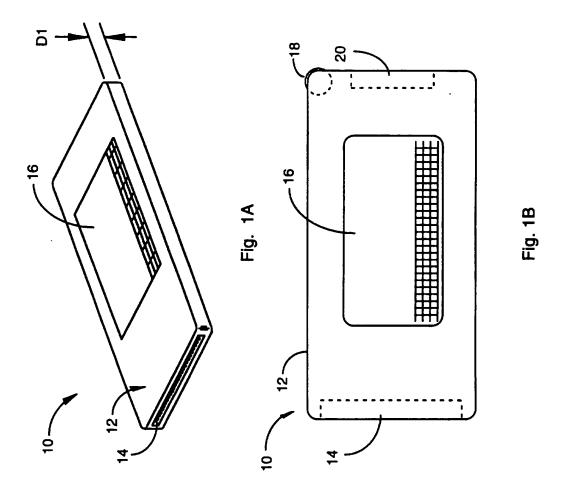
circuitry including an induction coil within the enclosure configured for sensing variations in the magnetic field and decoding the keystroke codes broadcast.

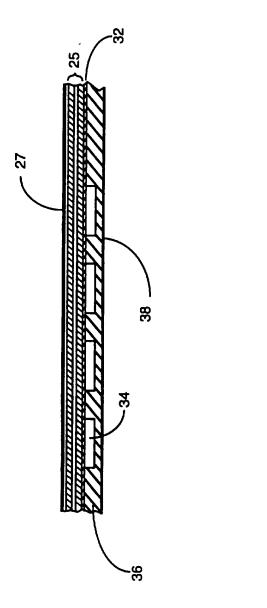
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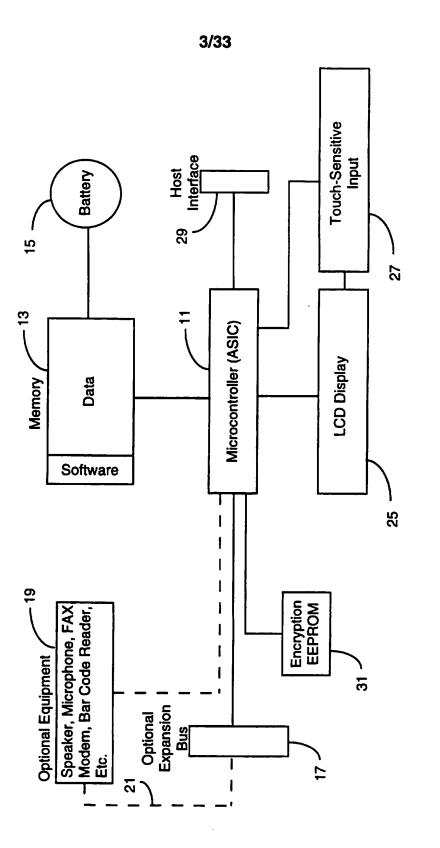
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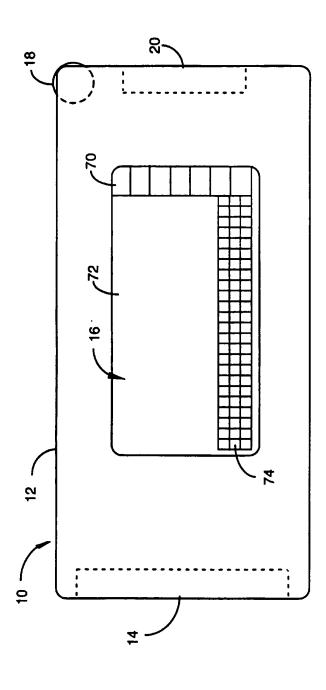
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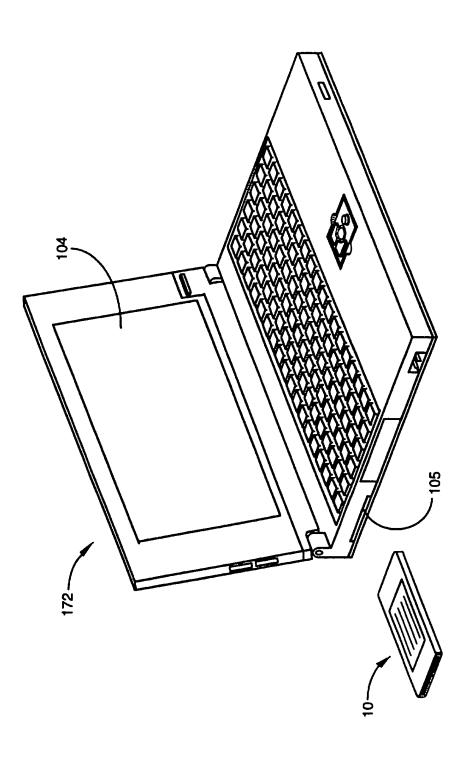


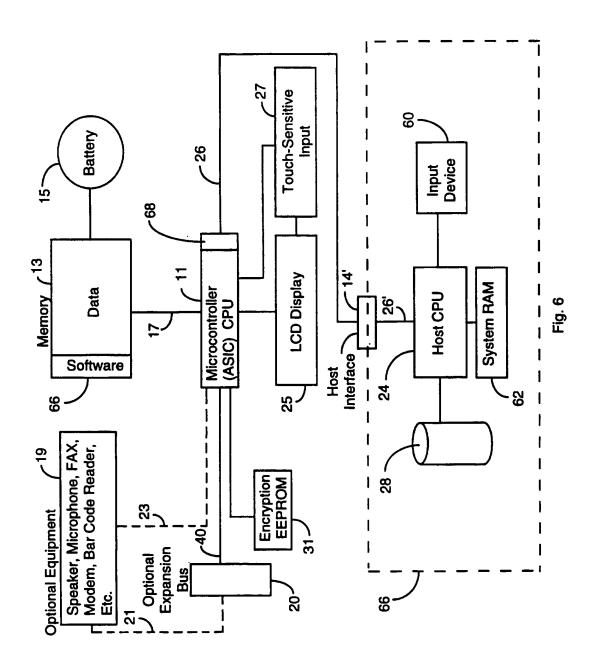


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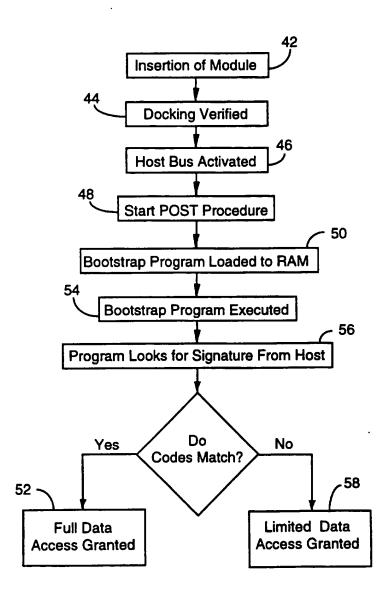


Fig. 7

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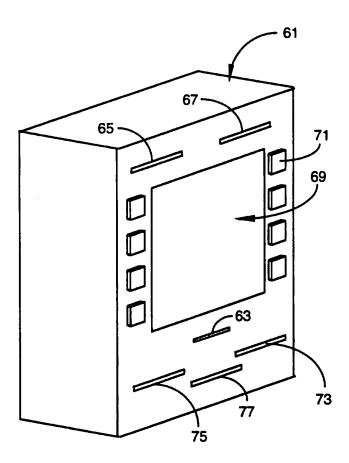
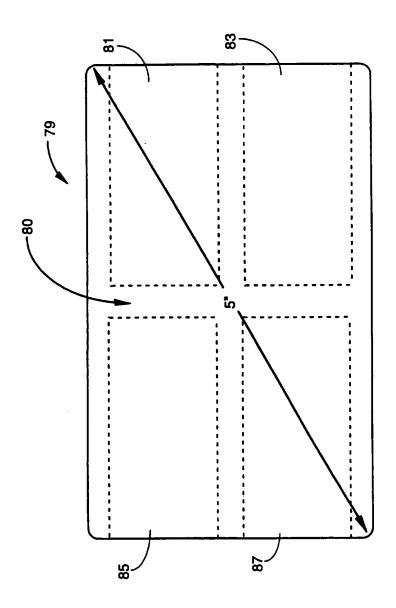


Fig. 8

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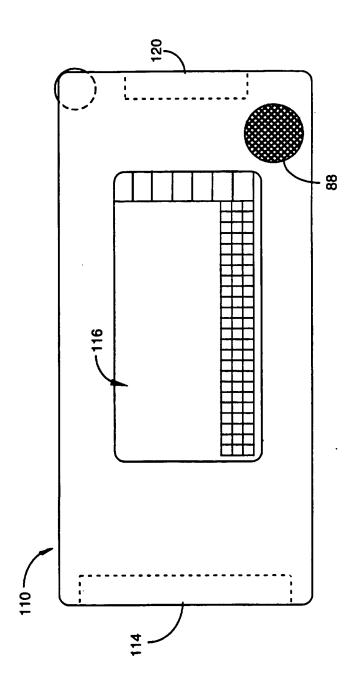
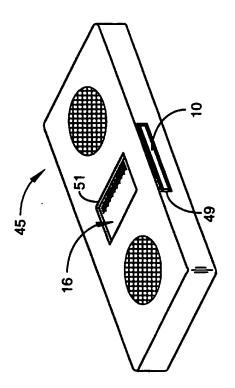
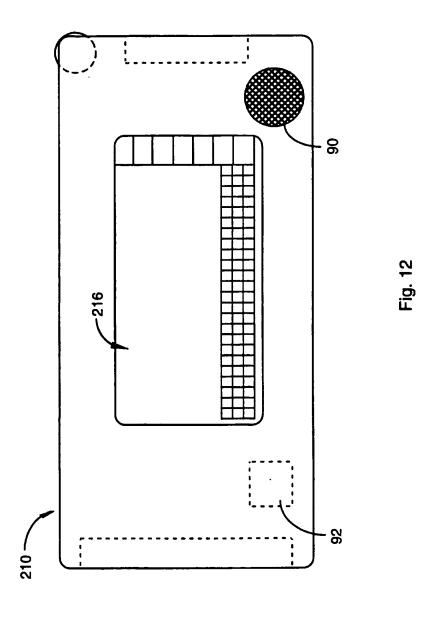


Fig. 10

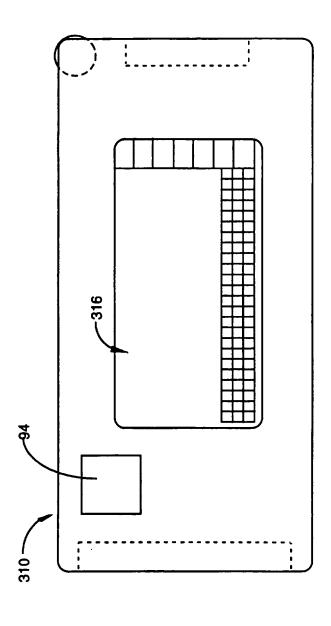
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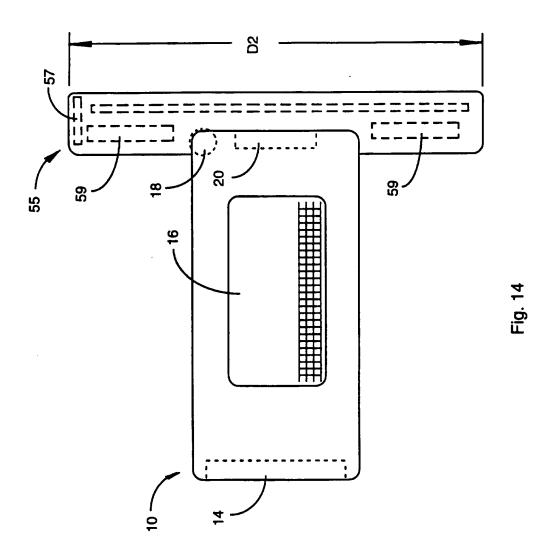
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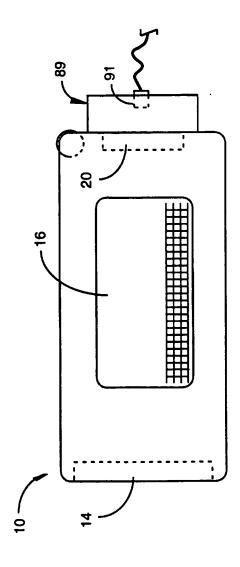
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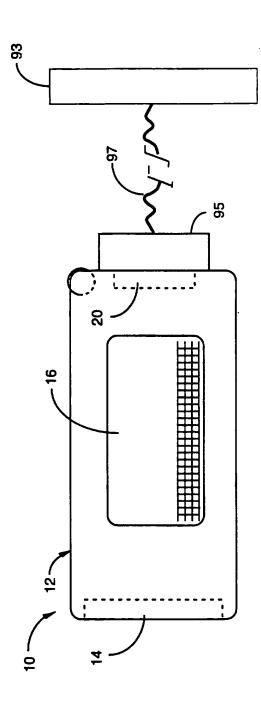
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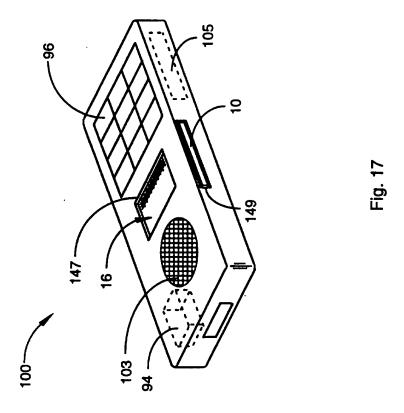
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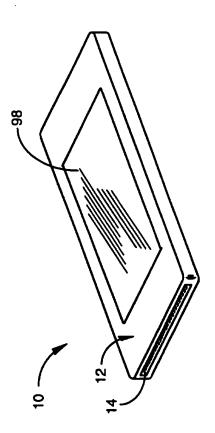
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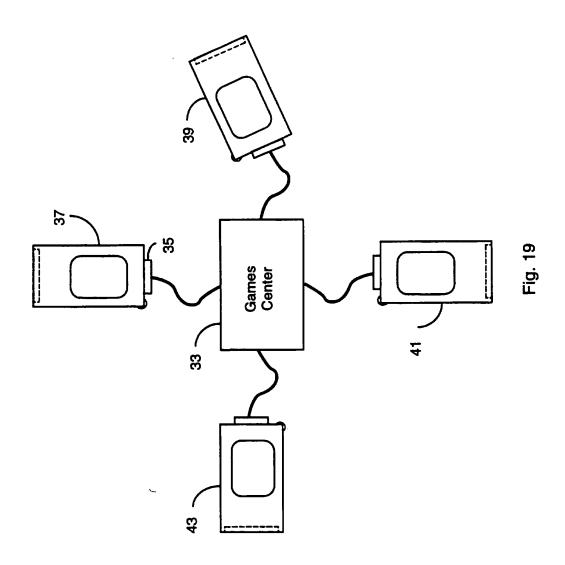


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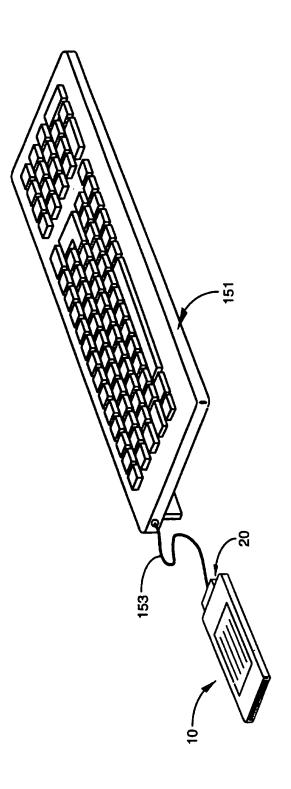
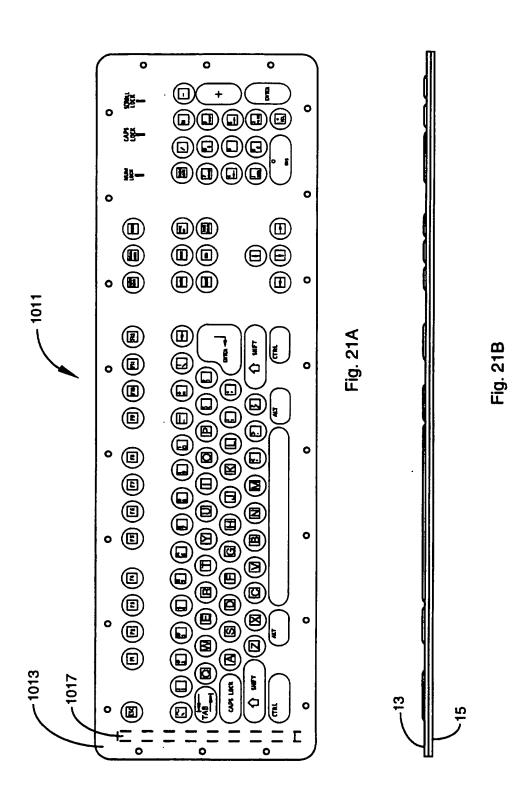
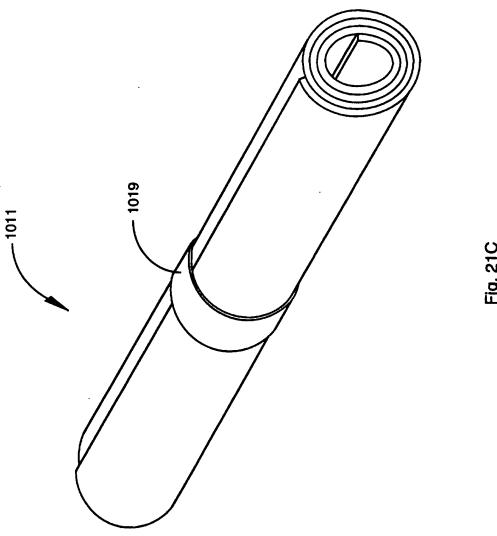


Fig. 20

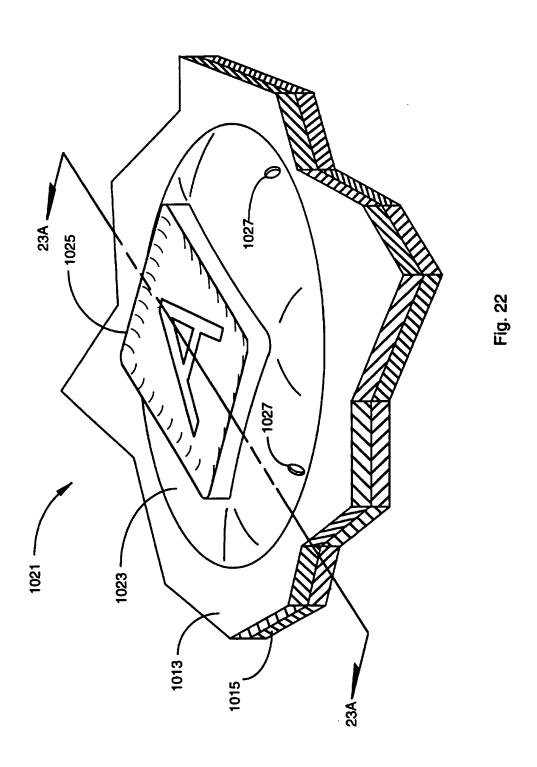
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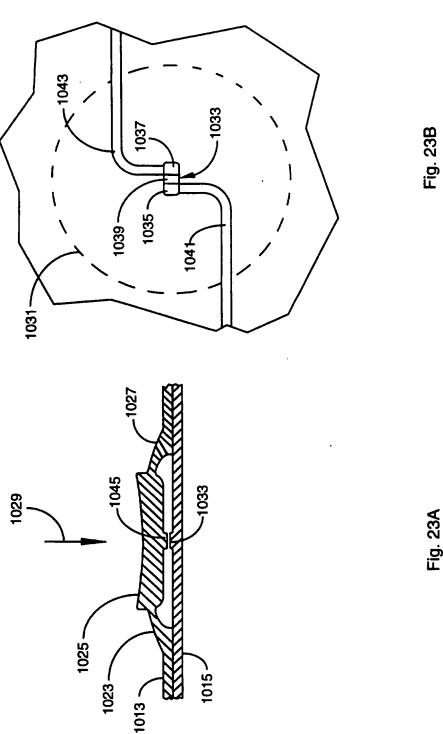
-19. ZIC

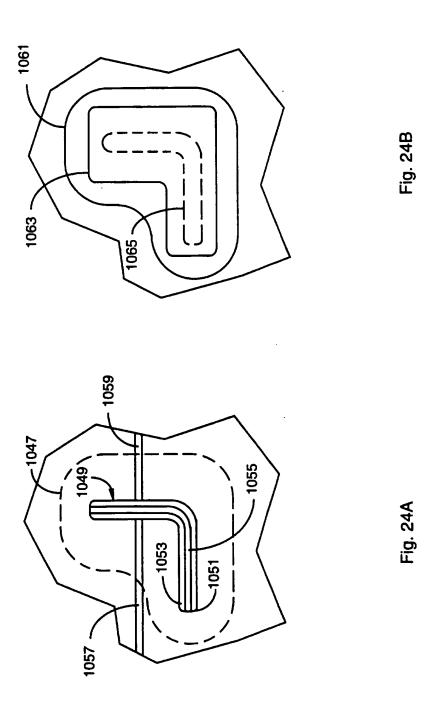
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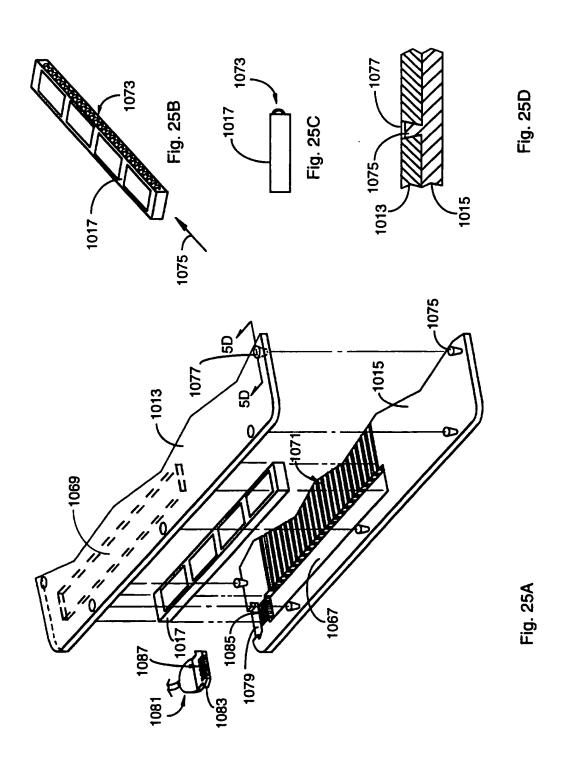
PCT/US95/09298 WO 96/02879

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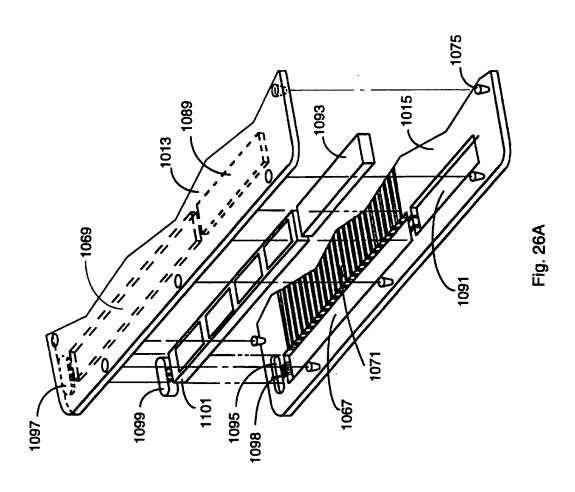


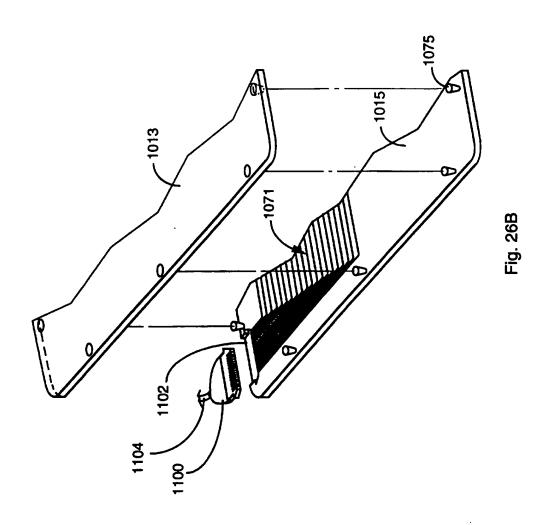


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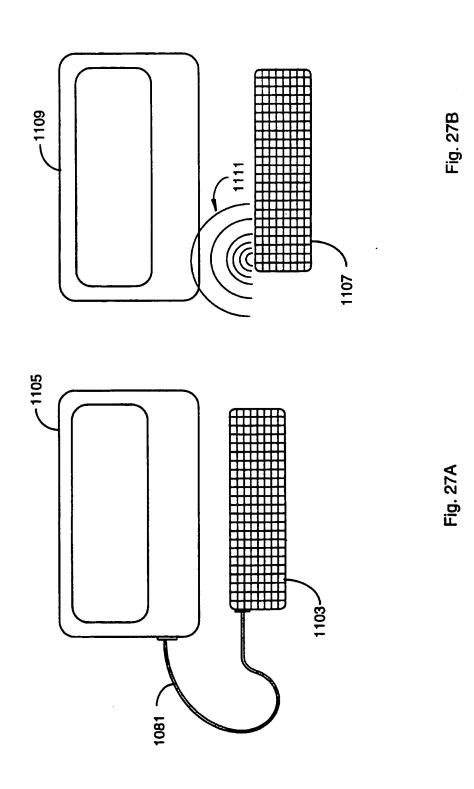


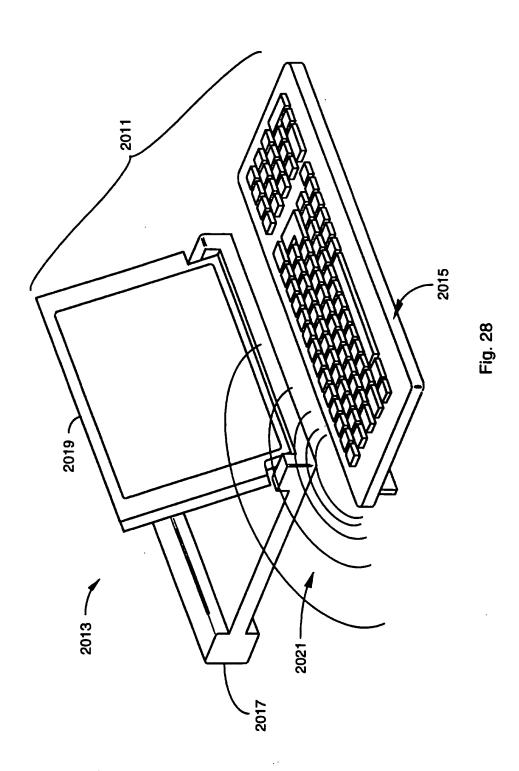
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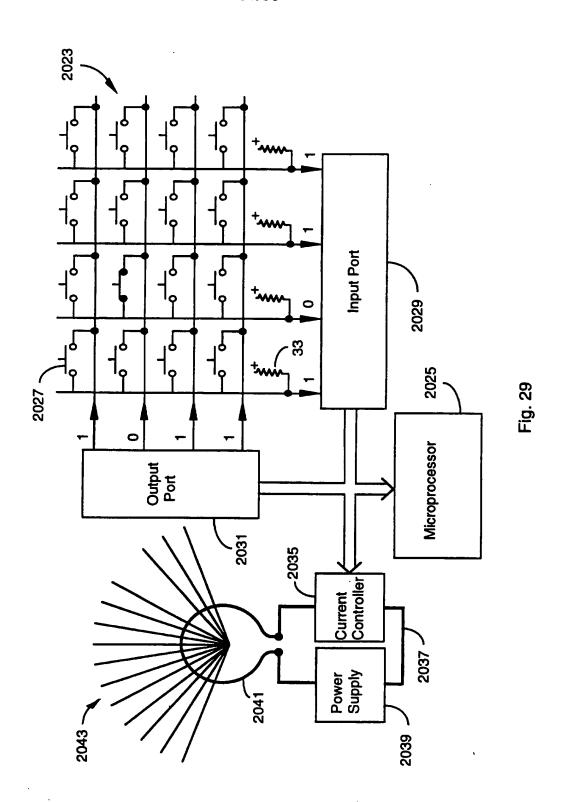


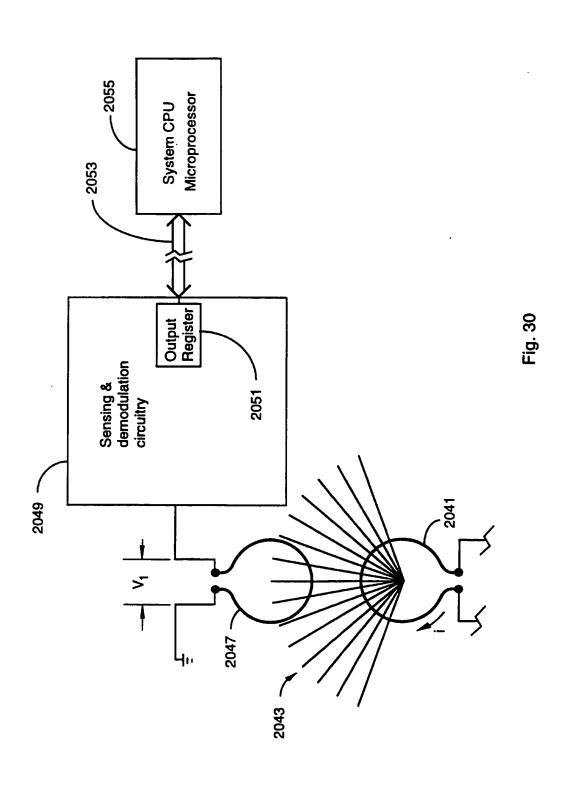
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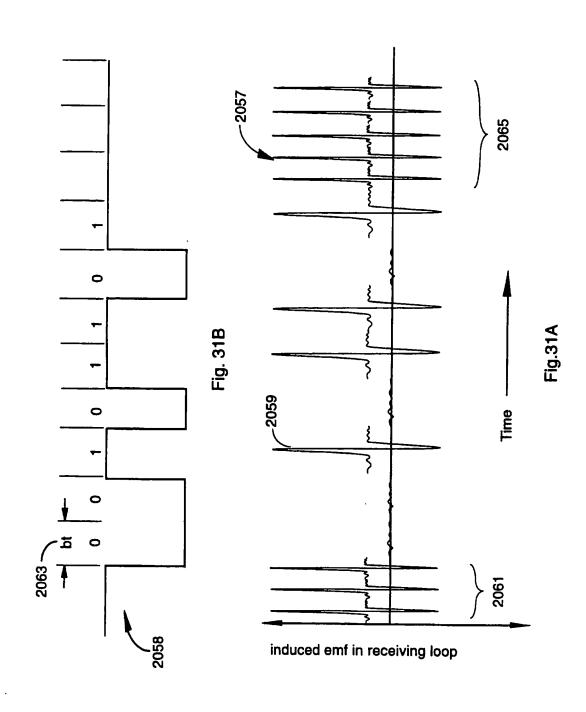




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INTERNATIONAL SEARCH REPORT

International application No. PCT/US95/09298

A. CLASSIFICATION OF SUBJECT MATTER IPC(6) :G06F 3/023, 15/163, 15/17; H05K 5/04; H04L 1/00					
US CL :Please See Extra Sheet.					
According to International Patent Classification (IPC) or to both national classification and IPC					
	DS SEARCHED				
Minimum d	ocumentation searched (classification system follower	d by classification symbols)			
U.S. : :	395/200.2, 280, 800, 275, 425, 155; 345/160, 169,	173; 379/93, 96; 364/709.08; 320/21			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched					
Electronic d	late base consulted during the international search (no	ame of data base and, where practicable	search terms used)		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Please See Extra Sheet.					
C. DOC	UMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where a	ppropriate, of the relevant passages	Relevant to claim No.		
Y	US, A, 4,545,023 (Mizzi) 01 Octo 1-2.	ber 1985, Fig. 4 and col.s	1		
Y	US, A, 5,220,521 (Kikinis) 15 Jul 2, lines 15-56.	ne 1993, Fig. 1A and col.	1		
Y	US, A, 4,534,012 (Yokozawa) 06 67-col. 2, line 63.	August 1985, col. 1, line	1		
Y	US,A, 4,748,656 (Gibbs et al.) 31 2, lines 8-41.	May 1988, Fig. 1 and col.	1		
Υ .	US, A, 5,029,183 (Tymes) 02 Jul line 56-col. 4, line 4.	y 1991, Fig. 1 and col. 2,	1		
Y	US, A, 5,227,957 (Deters) 13 Jul 3, line 40.	y 1993, col. 2, line 4-col.	1		
X Further documents are listed in the continuation of Box C. See patent family annex.					
Special categories of cited documents: "I" later document published after the international filing date or priority					
	cument defining the general state of the art which is not considered be part of particular relevance	date and not in conflict with the applica principle or theory underlying the inve	stion but cited to understand the section		
	tior document published on or after the international filing date	"X" document of particular relevance; the			
"L" doc	current which may throw doubts on priority claim(s) or which is	considered novel or cannot be consider when the document is taken alone	reu 10 MVOEVE AR MVERZIVE SECP		
	ed to establish the publication date of another citation or other cial reason (as specified) remost referring to an aral disclosure, was, exhibition or other	"Y" document of particular relevance; the considered to involve an inventive combined with one or more other such	step when the document is		
	ness	being obvious to a person skilled in th	e ert		
the priority date chained					
Date of the actual completion of the international search 21 AUGUST 1995 Date of mailing of the international search report 25 SEP 1995					
Name and mailing address of the ISA/US Authorized officer					
Commissioner of Petects and Trademarks					
Washington, D.C. 20231					
Facsimile No. (703) 308-5358 Telephone No. (703) 305-9647					

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US95/09298

C (Continua	tion). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages		Relevant to claim No.
Y	US, A,-4,163,138 (Harden) 31 July 1979, col. 2, lines 5-51.		1
Y	US, A, 4,644,326 (Villalobos et al.) 17 February 1987, col. 1, line 52-col. 3, line 25.		1
Y	US, A, 4,785,226 (Fujisawa et al.) 15 November 1988, c lines 22-49.	ol. 1,	1
Y	US, A, 5,065,357 (Shiraishi et al) 12 November 1991, co 34-col. 2, line 8.	ol. 1, line	1
Y	US, A, 5,157,585 (Myers) 20 October 1992, Fig. 1 and c line 57-col. 3, line 61.	col. 2,	1
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Form PCT/ISA/210 (continuation of second sheet)(July 1992)*

INTERNATIONAL SEARCH REPORT

International application No.

	PC170393109298				
A. CLASSIFICATION OF SUBJECT MATTER: US CL :					
395/200.2, 280, 800, 275, 425, 155; 345/160, 169, 173; 379/93, 96; 364/709.08; 320/21					
B. FIELDS SEARCHED Electronic data bases consulted (Name of data base and where practicable terms used	1):				
APS: digital assistant#, broadcast?, flexible keyboard#, keystroke# (p) (magnetic field), display?, monitor#, data terminal#, terminal#, CRT, controller#, microcontroller# or micro(w)controller#, interfac?, bus?, power supply, processor# or microprocessor# or micro(w)processor#					
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